

ECMA-40, 3rd Edition, HDLC Frame Structure

SCOPE AND CONFORMANCE

This Standard ECMA-40 defines the frame structure for bit sequence independent data transmission. This structure is intended for use in High-Level Data Link Control (HDLC) applications.

This Standard defines in detail the relative positions of the various components of the basic frame, the bit combination of the frame delimiting sequence, the mechanism used to obtain total transparency within the frame and the frame checking sequence used.

Other standards define the details of the address and control fields and the procedures used during the transmission.

Conformance with this Standard implies satisfying all the requirements of sections 4,5,6,7 and 8 with either the normal address field or the extended address field specified in 4.3, with either a normal or an extended control field as specified in 4.4 and with or without the optional information field specified in 4.5.