Near Field Communication Interface and Protocol (NFCIP-1)
Standard
ECMA-340
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Near Field Communication Interface and Protocol (NFCIP-1)
Brief history

This Standard specifies the interface and protocol for simple wireless communication between close coupled devices. These Near Field Communication (NFC) devices communicate with data rates of 106, 212, and 424 kbps.

This NFC Interface and Protocol (NFCIP-1) standard allows, but does not specify, applications in network products and consumer equipment.


This Ecma Standard has been adopted by the General Assembly of December 2004.
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1 Scope

This Standard defines communication modes for Near Field Communication Interface and Protocol (NFCIP-1) using inductive coupled devices operating at the centre frequency of 13,56 MHz for interconnection of computer peripherals. It also defines both the Active and the Passive communication modes of Near Field Communication Interface and Protocol (NFCIP-1) to realize a communication network using Near Field Communication devices for networked products and also for consumer equipment. This Standard specifies, in particular, modulation schemes, codings, transfer speeds, and frame format of the RF interface, as well as initialization schemes and conditions required for data collision control during initialization. Furthermore, this Standard defines a transport protocol including protocol activation and data exchange methods.

Information interchange between systems also requires, at a minimum, agreement between the interchange parties upon the interchange codes and the data structure.

2 Conformance

In addition to passing all normative tests and meeting all requirements specified in ECMA-356 and ECMA-362, conforming implementations implement both the Active and the Passive communication modes and meet all other requirements specified in ECMA-340.

3 Normative references

ECMA-356 NFCIP-1 - RF Interface Test Methods
ECMA-362 NFCIP-1 - Protocol Test Methods
ITU-T V.41:1988 Code-independent error-control system

4 Terms and definitions

For the purposes of this Standard, the following definitions apply.

4.1 Active communication mode
Move in which both the Initiator and the Target use their own RF field to enable the communication

4.2 ASK modulation
Amplitude Shift Keying, in which the amplitude of the carrier frequency is modulated according to the logic of the data to be transmitted

NOTE
The degree of modulation is expressed by \( \frac{a - b}{a + b} \times 100 \% \), where \( a \) and \( b \) respectively represent the maximum and minimum amplitudes of the modulated signal waveform.

4.3 Binary Coded Decimal (BCD)
A system for representing each of the decimal numbers 0 to 9 by a four-bit binary code

NOTE
The bits, from left to right, are worth 8, 4, 2 and 1 respectively in decimal, so for example the number 6 in BCD is 0110.

4.4 Collision
Transmission by two or more Targets or Initiators during the same time period, such that the Initiator or the Target is unable to distinguish from which Target the data originated
4.5 **Frame**
Sequence of data bits and optional error detection bits, with frame delimiters at start and end

4.6 **HThreshold**
The minimum value of an external RF field that a NFCIP-1 device shall detect in order not to disturb ongoing communication by ensuring that its own RF field is switched off

4.7 **Initiator**
Generator of the RF field and starter of the NFCIP-1 communication

4.8 **Load modulation**
Process of amplitude modulating a radio frequency field by varying the properties of a resonant circuit placed within the radio frequency field

4.9 **lsb first**
least significant bit first, indicating a serial data transmission system that sends lsb before all other bits

4.10 **LSB first**
Least Significant Byte first, indicating a serial data transmission system that sends LSB before all other bytes

4.11 **Manchester coding**
Method of bit coding whereby a logic level during a bit duration is represented by a sequence of two defined physical states of a communication medium

*NOTE*
The order of the physical states within the sequence defines the logical state. The coding system which divides into half at the changing point in the middle point of bit self-sustaining time, and makes the direction of the changes correspond to two logic value.

4.12 **Modulation index**
Defined as \( (a - b)/(a + b) \) where \( a \) and \( b \) are the peak and the minimum signal amplitude respectively with the value of the index possibly expressed as a percentage

*NOTE*
When the maximum amplitude of the modulated signal waveform is set to \( a \) and the minimum value is set to \( b \), the degree of abnormal conditions is usually expressed as a percent.

4.13 **msb first**
most significant bit indicating a serial data transmission system that sends the msb before all other bits

4.14 **MSB first**
Most Significant Byte indicating a serial data transmission system that sends the MSB before all other bytes

4.15 **NFCIP-1 device**
General term for either an Initiator or a Target communicating in the Active or the Passive communication mode

4.16 **NFC Identifier (NFCIDn)**
A randomly generated number used by the RF Collision Avoidance and Single Device Detection sequence for both the Active and the Passive communication modes
4.17 Passive communication mode
When the Initiator is generating the RF field and the Target responds to an Initiator command in a load modulation scheme

4.18 RF Collision Avoidance (RFCA)
Method to detect the presence of a RF field based on the carrier frequency and method to detect and resolve collisions on protocol level

4.19 SEL_PAR
Total number of valid bits of NFCID1 CLn including SEL_CMD and SEL_PAR transmitted by the Initiator

4.20 Sensing
An NFCIP-1 device in the Active communication mode expecting a Response to a Request it has sent on the RF field to detect the start of communication to receive the Request

4.21 Single Device Detection (SDD)
An algorithm used by the initiator to detect one out of several Targets in its RF field

4.22 Subcarrier
Signal of frequency (fs) used to modulate a carrier of frequency (fc)

4.23 Target
Responds to Initiator command either using load modulation scheme (RF field generated by Initiator) or using modulation of self generated RF field

4.24 Time Period
Defines the number of slots used for RF Collision Avoidance

4.25 Time Slot
Method of preparing a time window when a Target answers, and assigning and identifying two or more logic channels

4.26 Transaction
Includes the initialization and the transparent data exchange between an Initiator and a Target either in the Active or the Passive communication mode

5 Conventions and notations

5.1 Representation of numbers
The following conventions and notations apply in this document unless otherwise stated.

- Letters and digits in parentheses represent numbers in hexadecimal notation.
- The setting of bits is denoted by ZERO or ONE.
- Numbers in binary notation and bit patterns are represented by strings of digits 0 and 1 shown with the most significant bit to the left. Within such strings, X may be used to indicate that the setting of a bit is not specified within the string.

5.2 Names
The names of basic elements, e.g. specific fields, are written with a capital initial letter.
6 Acronyms

ALL_REQ  Wake up ALL Request
ASK     Amplitude Shift Keying
ATR     Attribute Request and Attribute Response
ATR_REQ Attribute Request
ATR_RES Attribute Response
BCC     NFCID1 CLn check byte, calculated as exclusive-or over the 4 previous bytes
BCD     Binary Code Decimal
bd      Bit duration
BRi     Receiving bit duration supported by Initiator
BRt     Receiving bit duration supported by Target
BSi     Sending bit duration supported by Initiator
BST     Sending bit duration supported by Target
CLn     Cascade Level n, 3 ≥ n ≥ 1
CMD     Command
CRC     Cyclic Redundancy Check
CT      Cascade Tag
D       Divisor
DEP     Data Exchange Protocol Request and Data Exchange Protocol Response
DEP_REQ Data Exchange Protocol Request
DEP_RES Data Exchange Protocol Response
DIDi    Initiator Device ID
DIDt    Target Device ID
DRi     Data rate Received by Initiator
DRt     Data rate Received by Target
DSi     Data rate Send by Initiator
DSL     Deselect Request and Deselect Response
DSL_REQ Deselect Request
DSL_RES Deselect Response
DSt     Data rate Send by Target
fc      Frequency of operating field (carrier frequency)
fd      Baseband frequency of Manchester coding
FRT     Frame Response Time
fs      Frequency of subcarrier (fc/16)
Gi      Optional information field for Initiator
Gt      Optional information field for Target
ID      Identification number
lsb     least significant bit
LSB     Least Significant Byte
MI      Multiple Information link for Data Exchange Protocol
msb     most significant bit
MSB     Most Significant Byte
NAD     Node Address
NFCID1  Random Identifier for single device detection in the Passive communication mode at 106 kbps
nfcid1n Byte number n of NFCID1
NFCID2  Random ID for SDD in the Passive communication mode at 212 kbps and 424 kbps
nfcid2n Byte number n of the Random Identifier NFCID2
NFCID3  Random ID for transport protocol activation
nfcid3n Byte number n of the Random Identifier NFCID3
P       Odd parity bit
PA      Preamble
pdu     protocol data unit
PFB     Control information for transaction
PNI     Packet Number Information
PPI     Protocol Parameters used by Initiator
PPT     Protocol Parameters used by Target
7 General

This Standard defines both the Active and the Passive communication modes as follows:

In the Active communication mode, both the Initiator and the Target shall use their own RF field to enable communication. The Initiator starts the NFCIP-1 communication. The Target responds to an Initiator command in the Active communication mode using self-generated modulation of self-generated the RF field.

In the Passive communication mode, the Initiator generates the RF field and starts the communication. The Target responds to an Initiator command in the Passive communication mode using a load modulation scheme.

The communication over the RF interface in the Active and the Passive communication mode shall include modulation schemes, transfer speed and bit coding. In addition it shall include the start of communication, the end of communication, the bit and byte representation, the framing and error detection, the single device detection, the protocol and parameter selection and the data exchange and de-selection of Near Field Communication Interface and Protocol (NFCIP-1) devices.

All NFCIP-1 devices shall have communication capability on 106 kbps and may switch to another transfer speed or stay at 106 kbps. All NFCIP-1 devices shall have communication capability on 212 kbps and may switch to another transfer speed or stay at 212 kbps. All NFCIP-1 devices shall have communication capabilities on 424 kbps and may switch to another transfer speed or stay at 424 kbps.

The mode (Active or Passive) shall not be changed during one transaction until the deactivation of the Target or removal of the Target, even though the transfer speed of Initiator to Target and the transfer speed of the Target to the Initiator may not be the same. The change of transfer speed during one transaction may be performed by a parameter change procedure.

The transaction is started by device initialisation and terminated by device de-selection (or equivalent).
8 RF field

The carrier frequency of the RF field (fc) shall be 13,56 MHz.
The minimum unmodulated RF field shall be $H_{\text{min}}$ and has a value of 1,5 A/m rms.
The maximum unmodulated RF field shall be $H_{\text{max}}$ and has a value of 7,5 A/m rms.
This field shall be modulated during communication.

8.1 Passive Communication Mode
An Initiator shall produce a RF field to energise the target.
A Target shall operate continuously between $H_{\text{min}}$ and $H_{\text{max}}$.

8.2 Active Communication Mode
An Initiator and a Target shall alternately generate a RF field of at least $H_{\text{min}}$ and not exceeding $H_{\text{max}}$ at manufacturer specified positions (operating volume).

8.3 External RF field threshold value
NFCIP-1 devices shall detect external RF fields at 13,56MHz with a value higher than $H_{\text{Threshold}}$ while performing external RF field detection.
The threshold value is $H_{\text{Threshold}} = 0,1875$ A/m.

9 RF Signal Interface

9.1 Bit duration
The bit duration $bd$ is calculated by the following formula:
$1 \frac{bd}{(D \times fc)}$
The values of the divisor D depend on the bit rate and are given by Table 1. The fc is the carrier frequency as defined in clause 8.

<table>
<thead>
<tr>
<th>Communication Mode</th>
<th>kbps</th>
<th>Divisor D</th>
</tr>
</thead>
<tbody>
<tr>
<td>active or passive</td>
<td>106</td>
<td>1</td>
</tr>
<tr>
<td>active or passive</td>
<td>212</td>
<td>2</td>
</tr>
<tr>
<td>active or passive</td>
<td>424</td>
<td>4</td>
</tr>
<tr>
<td>Active</td>
<td>847</td>
<td>8</td>
</tr>
<tr>
<td>Active</td>
<td>1695</td>
<td>16</td>
</tr>
<tr>
<td>Active</td>
<td>3390</td>
<td>32</td>
</tr>
<tr>
<td>Active</td>
<td>6780</td>
<td>64</td>
</tr>
</tbody>
</table>

NOTE
The Initiator for starting the communication chooses the initial bit rate.

9.2 Active communication mode
The specification of both from the Initiator to the Target and from the Target to the Initiator shall be identical.
9.2.1 106 kbps

9.2.1.1 Bit rate

The bit rate for the transmission during initialisation and single device detection shall be fc/128 (106 kbps).

9.2.1.2 Modulation

Communication from the Initiator to a Target and a Target to the Initiator for a bit rate of fc/128 shall use the modulation principle of ASK 100 % of the RF operating field to create a “Pulse” as shown in Figure 1.

The envelope of the field shall decrease monotonically to less than 5 % of its initial value H\textsubscript{INITIAL} and remain less than 5 % for more than t2. (See Table 2.) This envelope shall comply with Figure 1.

If the envelope of the field does not decrease monotonically, the time between a local maximum and the time of passing the same value before the local maximum shall not exceed 0.5 µs. This shall only apply if the local maximum is greater than 5 % of H\textsubscript{INITIAL}.

Overshoots shall remain within 90 % and 110 % of H\textsubscript{INITIAL}.

The Target shall detect the “End of Pulse” after the field exceeds 5 % of H\textsubscript{INITIAL} and before it exceeds 60 % of H\textsubscript{INITIAL}. The “End of Pulse” is defined by t4 in Table 2. This definition applies to all modulation envelope timings.

Figure 1 — Pulse shape
Table 2 — Pulse shape value

<table>
<thead>
<tr>
<th>Pulses length (Condition)</th>
<th>t1 [µs]</th>
<th>t2 [µs]</th>
<th>t3 [µs]</th>
<th>t4 [µs]</th>
</tr>
</thead>
<tbody>
<tr>
<td>Maximum</td>
<td>3,0</td>
<td>t1</td>
<td>1,5</td>
<td>0,4</td>
</tr>
<tr>
<td>Minimum</td>
<td>2,0</td>
<td>0,7</td>
<td>0,5</td>
<td>0,0</td>
</tr>
</tbody>
</table>

9.2.1.3 Bit representation and coding
The following coding shall be used:
- Start of communication: at the beginning of the bit duration a “Pulse” shall occur.
- ONE: after a time of half the bit duration a “Pulse” shall occur.
- ZERO: For the full bit duration no modulation shall occur with the following two exceptions:
  - If there are two or more contiguous ZEROs, from the second ZERO on a Pulse shall occur at the beginning of the bit duration.
  - If the first bit after a “start of communication” is ZERO, a ‘Pulse’ shall occur at the beginning of the bit duration.
- End of Communication: ZERO followed by one bit duration without modulation.
- No information: shall be coded with at least two full bit duration without modulation.

9.2.1.4 Byte encoding
The byte encoding shall be least significant bit (lsb) first.

9.2.2 212 kbps and 424 kbps
9.2.2.1 Bit rate
The bit rates for the transmission during initialisation and single device detection shall respectively be fc/64 (212 kbps) or fc/32 (424 kbps).

9.2.2.2 Modulation
The Initiator and the Target shall use the modulation of ASK with the modulation index of 8 % to 30 % of the operating field. The modulation waveform shall comply with Figure 2. The rising and falling edges of the modulation shall be monotonic. The modulation for the transmission during initialisation and single device detection shall be the same. a and b define the peak and the minimum signal amplitude. See 4.11.

Table 3 — Modulated waveform

<table>
<thead>
<tr>
<th></th>
<th>212 kbps</th>
<th>424 kbps</th>
</tr>
</thead>
<tbody>
<tr>
<td>tf</td>
<td>2,0 µs max</td>
<td>1,0 µs max</td>
</tr>
<tr>
<td>tr</td>
<td>2,0 µs max</td>
<td>1,0 µs max</td>
</tr>
<tr>
<td>y</td>
<td>0,1 (a – b)</td>
<td>0,1 (a – b)</td>
</tr>
<tr>
<td>hf, hr</td>
<td>0,1 (a – b) max</td>
<td>0,1 (a – b) max</td>
</tr>
</tbody>
</table>
9.2.2.3 Bit representation and coding

Manchester bit encoding shall be employed. The waveform is shown in Figure 3 and Figure 4. Bit coding format is Manchester with logic levels defined as:

Logic “ZERO”: The first half of a bit is carrier low field amplitude, and the second half of the bit shall be carrier high field amplitude (no modulation applied).

Logic “ONE”: The first half of a bit is carrier high field amplitude (no modulation applied), and the second half of the bit shall be carrier low field amplitude.

Reverse polarity in amplitude shall be permitted. Polarity shall be detected from the SYNC.

![Figure 3 — Manchester bit encoding (obverse amplitude)](image)

![Figure 4 — Manchester bit encoding (reverse amplitude)](image)
9.2.2.4 **Byte encoding**
The byte encoding shall be most significant bit (msb) first.

9.3 **Passive communication mode**

9.3.1 106 kbps Initiator to Target

9.3.1.1 **Bit rate**
The bit rate for transmission during initialisation and single device detection from the Initiator to the Target in the Passive communication mode shall be the same as the bit rate for communication from Initiator to the Target in the Active communication mode. See 9.2.1.1.

9.3.1.2 **Modulation**
The modulation for transmission during initialisation and single device detection from the Initiator to the Target in the Passive communication mode shall be the same as the modulation for communication from Initiator to the Target in the Active communication mode. See 9.2.1.2.

9.3.1.3 **Bit representation and coding**
The bit representation and coding for the transmission during initialisation and single device detection from the Initiator to the Target in the Passive communication mode shall be the same as the bit representation and coding for communication from Initiator to the Target in the Active communication mode. See 9.2.1.3.

9.3.1.4 **Byte encoding**
The byte encoding shall be least significant bit (lsb) first. See 9.2.1.4.

9.3.2 106 kbps Target to Initiator

9.3.2.1 **Bit rate**
The bit rate for the transmission during initialisation and single device detection shall be fc/128.

9.3.2.2 **Modulation**
The Target shall respond to the Initiator via an inductive coupling area where the carrier frequency is loaded to generate a subcarrier with frequency fs. The subcarrier shall be generated by switching a load in the Target.

The load modulation amplitude shall be at least 30/H^{1.2} (mV peak) where H is the (rms) value of magnetic field strength in A/m.

9.3.2.3 **Subcarrier Frequency**
The frequency fs of the subcarrier shall be fc/16.

9.3.2.4 **Subcarrier modulation**
Every bit period shall start with a defined phase relation to the subcarrier. The bit period shall start with the loaded state of the subcarrier.

The subcarrier shall be modulated with the sequences defined in clause 9.3.2.5.

9.3.2.5 **Bit representation and coding**
The Bit representation and coding is defined in clause 9.2.2.3 and shown in Figure 3 Manchester Coding with obverse amplitude. Reverse polarity in amplitude shall be not allowed.

9.3.2.6 **Byte encoding**
The byte encoding shall be least significant bit (lsb) first.

9.3.3 212 kbps and 424 kbps Initiator to Target

9.3.3.1 **Bit rate**
The bit rate for transmission during initialisation and single device detection from the Initiator to the Target in the Passive communication mode shall be the same as the bit rate for communication from Initiator to the Target in the Active communication mode. See 9.2.2.1.
9.3.3.2 Modulation
The modulation for transmission during initialisation and single device detection from the Initiator to the Target in the Passive communication mode shall be the same as the modulation for communication from Initiator to the Target in the Active communication mode. See 9.2.2.2.

9.3.3.3 Bit representation and coding
The bit representation and coding for the transmission during initialisation and single device detection from the Initiator to the Target in the Passive communication mode shall be the same as the bit representation and coding for communication from Initiator to the Target in the Active communication mode. See 9.2.2.3.

9.3.3.4 Byte encoding
The byte encoding is defined in clause 9.2.2.4.

9.3.4 212 kbps and 424 kbps Target to Initiator
9.3.4.1 Bit rate
The bit rate for transmission during initialisation and single device detection from the Initiator to the Target in the Passive communication mode shall be the same as the bit rate for communication from Initiator to the Target in the Active communication mode. See 9.2.2.1.

9.3.4.2 Modulation
The Target shall be capable of communication to the Initiator via an inductive coupling area where the carrier frequency is loaded to generate a Manchester coding with bit duration $bd$. (See 9.2.) The Manchester coding shall be generated by switching a load in the Target.
The load modulation amplitude shall be at least $30/H^{1.2}$ (mV peak) where $H$ is the (rms) value of magnetic field strength in A/m.

9.3.4.3 Bit representation and coding
The bit representation and coding for the transmission during initialisation and single device detection from the Target to the Initiator in the Passive communication mode shall be the same as the bit representation and coding for communication in the Active communication mode. See 9.2.2.3.

9.3.4.4 Byte encoding
The byte encoding is defined in clause 9.2.2.4.

10 General Protocol flow
The General Protocol flow between NFCIP-1 devices shall be conducted through the following consecutive operations:

- Any NFCIP-1 device shall per default be in Target mode.
- When in Target mode, it shall not generate an RF field, and shall wait silently for a command from the Initiator.
- The NFCIP-1 device may switch to Initiator mode only if required by the application.
- The application shall determine either Active or Passive communication mode and transfer speed.
- Initiator shall test for external RF field present and shall not activate its RF field if an external RF field is detected. See clause 8.3.
- If an external RF field is not detected, the Initiator shall activate its RF field.
- The Target shall be activated by the RF field of the Initiator.
- Transmission of a command by the Initiator either in the Active communication mode or in the Passive communication mode at a selected transfer speed.
Transmission of a response by the Target either in the Active communication mode or in the Passive communication mode. The communication mode and the transfer speed shall be the same as the Initiator communication mode and the transfer speed.

Figure 5 shows the general initialisation and single device detection flow for the Active and the Passive communication mode at different transfer speeds.

The General Protocol flow describes the flow to initialise and select the Targets either in the Passive communication mode or in the Active communication mode using one of the chosen transfer speeds. RF Collision Avoidance is described in clause 11.1. Passive communication mode is described in clause 11.2. The initialisation and SDD for 106 kbps is described in clause 11.2.1, initialisation and SDD for 212 kbps and 424 kbps is described in clause 11.2.2. The Active communication mode is described in clause 11.3.

The Activation of the Protocol is described in clause 12.5. The Parameter Selection is described in clause 12.5.3. The Data Exchange Protocol is described in clause 12.6. The Deactivation is described in clause 12.7.

11 Initialization

This section describes the initialization and collision detection protocol for Targets in the Active and the Passive communication mode. The Initiator shall detect a collision that occurs, when at least two Targets simultaneously transmit bit patterns with one or more bit positions where they transmit complementary values.

Figure 5 shows the general initialization and Single Device Detection flow for the Active and the Passive communication mode at different transfer speeds.

11.1 RF Collision Avoidance

In order not to disturb any other NFC communication and any current infrastructure running on the carrier frequency, an Initiator for NFC communication shall not generate its own RF field as long as another RF field is detected.

11.1.1 Initial RF Collision Avoidance

To start communication with the Target device either in the Active or the Passive communication mode an Initiator shall sense continuously for the presence of an external RF field. See clause 8.3.

If the Initiator detects no RF field within the timeframe $T_{DT} + n \times T_{RF}$ the RF field shall switch on. The integer value of $n$ is randomly generated. Figure 6 illustrates the initial RF Collision Avoidance during initialisation.
Figure 5 — General initialization and single device detection flow
Figure 6 — Initial RF Collision Avoidance

- **T\text{IDT}**: Initial delay time. $T\text{IDT} > 4096 / \text{fc}$
- **T\text{RFW}**: RF waiting time. $512 / \text{fc}$
- **n**: randomly generated number of Time Periods for $T\text{RFW}$.
  \[0 \leq n \leq 3\]
- **T\text{IRFG}**: Initial guard-time between switching on RF field and start to send command or data frame.
  \[T\text{IRFG} > 5 \text{ ms}\]

The RF field, which is generated by the Initiator, shall be switched off in the Active communication mode. The RF field, which is generated by the Initiator, shall not be switched off in the Passive communication mode.

### 11.1.2 Response RF Collision Avoidance

In addition to the initial RF Collision Avoidance as described in clause 11.1.1. A response RF collision avoidance during activation shall be required in the Active communication mode to avoid collision of data by simultaneous responding of more than one target. Figure 7 illustrates the response RF Collision Avoidance sequence during initialisation.

**Figure 7 — Response RF Collision Avoidance sequence during activation**

- **T\text{ADT}**: Active delay time, sense time between RF off Initiator/Target and Target/Initiator.
  \[(768/\text{fc} \leq T\text{ADT} \leq 2559/\text{fc})\]
- **T\text{RFW}**: RF waiting time. $(512/\text{fc})$
- **n**: Randomly generated number of Time Periods for $T\text{RFW}$. $(0 \leq n \leq 3)$
T_{ARFG} : Active guard time between switching on RF field and start to send command. 
\( (T_{ARFG} > 1024/fc) \)

11.2 Passive communication mode

11.2.1 Initialisation and Single Device Detection at 106 kbps

11.2.1.1 Frame format and timing

This section defines the frame format and timing used during initialisation and Single Device Detection in passive communication mode at 106 kbps. For bit representation and coding, see 9.3.1.3.

Before the communication starts the initiator has to perform the initial RF Collision Avoidance as described in clause 11.1.1.

Data Frames shall be transferred in pairs, the Initiator initiates the communication followed by the response of the Target.

The Initiator Frame format includes the start of communication, the information and the end of communication. See Table 4.

Table 4 — Initiator Frame format

<table>
<thead>
<tr>
<th>Start of communication (Start)</th>
<th>Information</th>
<th>End of communication (End)</th>
</tr>
</thead>
</table>

− Frame Response time between Initiator and Target.
− The Target frame format includes the start of communication, the information and the end of communication. See Table 5.

Table 5 — Target Frame format

<table>
<thead>
<tr>
<th>Start of communication (Start)</th>
<th>Information</th>
<th>End of communication (End)</th>
</tr>
</thead>
</table>

− Frame Response time Target to Initiator.

The Frame Response time (FRT) from Target to Initiator overlaps the Initiator end of communication.

11.2.1.2 Frame Response Time Initiator to Target

The Frame Response Time is the time between the end of the last pulse transmitted by the Initiator and the first modulation edge within the start bit transmitted by the Target. Table 6 defines values for \( n \) and FRT depending on the command type and the logic state of the last transmitted data bit in this command.

Table 6 — Frame Response Time (Initiator to Target)

| Command type | \( n \) (integer value) | FRT
<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>SENS_REQ</td>
<td>9</td>
<td>( (n \times 128 + 84) / fc )</td>
</tr>
<tr>
<td>ALL_REQ</td>
<td></td>
<td>( (n \times 128 + 20) / fc )</td>
</tr>
<tr>
<td>SDD_REQ</td>
<td></td>
<td></td>
</tr>
<tr>
<td>SEL_REQ</td>
<td>≥ 9</td>
<td></td>
</tr>
<tr>
<td>All other commands</td>
<td>≥ 9</td>
<td></td>
</tr>
</tbody>
</table>

The value \( n = 9 \) means that all Targets in the field shall respond in a synchronous way which is needed for Single Device Detection. For all other commands the Target shall ensure that the first modulation edge within the start bit is aligned to the bit-grid according to the definition of Table 6.
11.2.1.3 Frame Response time Target to Initiator
This is the time between the last modulation transmitted by the Target and the first pause
transmitted by the Initiator and shall be at least 1 172 / fc.

11.2.1.4 Sense Guard Time
The Sense Guard Time is defined as the minimum time between the start bits of two
consecutive SENS_REQ commands. It has the value 7 000 / fc.

11.2.1.5 Frame formats
The following frame types shall be defined as:
− Short Frames for commands defined in 11.2.1.5.1.
− Standard Frames for regular commands in 11.2.1.5.2.
− Bit oriented Single Device Detection frame for SDD_REQ command in 11.2.1.5.3.

11.2.1.5.1 Short frame
A short frame shall be used to initiate communication and consist of, in the following order:
− Start of communication
− 7 data bits transmitted lsb first (for command coding see 11.2.1.16)
− End of communication
No parity bit is added.

<table>
<thead>
<tr>
<th>bit 0</th>
<th>bit 1</th>
<th>bit 2</th>
<th>bit 3</th>
<th>bit 4</th>
<th>bit 5</th>
<th>bit 6</th>
</tr>
</thead>
<tbody>
<tr>
<td>Start</td>
<td>Command</td>
<td>End</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Figure 8 — Short frame

11.2.1.5.2 Standard frame
Standard frames shall be used for data exchange and consist of, in the following order:
− Start of communication
− \( n \times (8\text{ data bits} + \text{odd parity bit}) \). With \( n \geq 1 \). The lsb of each byte shall be transmitted
first. See 11.2.1.16. Each byte shall be followed by an odd parity bit. The parity bit \( P \)
shall be set such that the number of \( \text{ONEs} \) is odd in (bit 0 to bit 7, \( P \)).
− End of communication.

<table>
<thead>
<tr>
<th>Byte 0</th>
<th>Byte 1</th>
<th>..</th>
<th>Byte n</th>
</tr>
</thead>
<tbody>
<tr>
<td>Start</td>
<td>bit 0</td>
<td>bit 1</td>
<td>..</td>
</tr>
<tr>
<td>Command or Data</td>
<td>Data</td>
<td>..</td>
<td>Data</td>
</tr>
</tbody>
</table>

Figure 9 — Standard Frame

11.2.1.5.3 Bit oriented Single Device Detection frame
A collision shall be detected when at least two Targets transmit different bit patterns to the
Initiator. In this case the carrier frequency shall be modulated with the subcarrier for the
whole bit duration for at least one bit.

Bit oriented SDD frames shall only be used during bit frame SDD and shall in fact for
standard frames with a length of 7 bytes, split into two parts:
− Part 1 for transmission from Initiator to Target.
− Part 2 for transmission from Target to Initiator.
For the length of part 1 and part 2, the following rules shall apply:

- Rule 1: The sum of data bits shall be 56.
- Rule 2: The minimum length of part 1 shall be 16 data bits.
- Rule 3: The maximum length of part 1 shall be 55 data bits.

Consequently, the minimum length of part 2 shall be 1 data bit and the maximum length shall be 40 data bits.

Since the split can occur at any bit position within a byte, two cases are defined:

- Case FULL BYTE: Split after a complete byte. A parity bit shall be added after the last data bit of Part 1.
- Case SPLIT BYTE: Split within a byte. No parity bit shall be added after the last data bit of Part 1. The first parity bit from Part 2 is undefined.

The following examples for case FULL BYTE and case SPLIT BYTE define the bit organisation and order of bit transmission.

These examples include proper values for SEL_PAR and BCC.

<table>
<thead>
<tr>
<th>Standard Frame, split after 3rd complete data byte</th>
</tr>
</thead>
<tbody>
<tr>
<td><img src="image-url" alt="Frame Diagram" /></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Single Device Detection, Part 1: Initiator to Target</th>
</tr>
</thead>
<tbody>
<tr>
<td><img src="image-url" alt="Initiator Diagram" /></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Single Device Detection, Part 2: Target to Initiator</th>
</tr>
</thead>
<tbody>
<tr>
<td><img src="image-url" alt="Target Diagram" /></td>
</tr>
</tbody>
</table>

*Figure 10 — Bit organisation and transmission of bit oriented single device detection frame, case FULL BYTE*
Standard Frame, split after 3rd complete byte + 4 data bits

<table>
<thead>
<tr>
<th>Byte 0</th>
<th>P</th>
<th>Byte 1</th>
<th>P</th>
<th>Byte 2</th>
<th>P</th>
<th>Byte 3</th>
<th>P</th>
<th>Byte 4</th>
<th>P</th>
<th>Byte 5</th>
<th>P</th>
<th>Byte 6</th>
<th>P</th>
</tr>
</thead>
<tbody>
<tr>
<td>Start</td>
<td>1</td>
<td>SEL_CMD (93) 0</td>
<td>nfcid10 (35) 1</td>
<td>00000100 (20) 0</td>
<td>nfcid12 (EF) 0</td>
<td>nfcid13 (AD) 0</td>
<td>BCC (75) 0</td>
<td>End</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Single Device Detection, Part 1: Initiator to Target

| Initiator to Target |
|---|---|---|---|---|
| Byte 0 | P | Byte 1 | P | Byte 2 | P | bits 0-3 | End |
| Start  | 1 | SEL_CMD (93) 0 | nfcid10 (35) 1 | 00000100 (20) 0 | nfcid12 (EF) 0 | nfcid13 (AD) 0 | BCC (75) 0 |

Single Device Detection, Part 2: Target to Initiator

<table>
<thead>
<tr>
<th>bits 4-7</th>
<th>P</th>
<th>Byte 4</th>
<th>P</th>
<th>Byte 5</th>
<th>P</th>
<th>Byte 6</th>
<th>P</th>
</tr>
</thead>
<tbody>
<tr>
<td>Start</td>
<td>0100 X</td>
<td>nfcid12 (EF) 0</td>
<td>nfcid13 (AD) 0</td>
<td>BCC (75) 0</td>
<td>End</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Figure 11 — Bit organisation and transmission of bit oriented single device detection frame, case SPLIT

For a SPLIT BYTE, the first parity bit of part 2 shall be ignored by the Initiator.

11.2.1.6 CRC for 106 kbps

The CRC for the Passive communication mode at 106 kbps is defined in A.1.

11.2.1.7 Target States

The following sections provide descriptions of the States for a Target specific to the bit collision detection protocol.

Targets shall react to valid received frames only. No response shall be sent when transmission errors are detected.

11.2.1.8 POWER-OFF State

In the POWER-OFF State, the Target shall not be powered by the RF field of the initiator due to a lack energy.

If the Target is in an energising magnetic field greater than $H_{\text{min}}$ it shall enter its SENSE State.

11.2.1.9 SENSE State

In the SENSE State, the Target is powered. It listens for commands and shall recognise SENS_REQ or ALL_REQ Commands.

The Target enters the RESOLUTION State when it has received a valid SENS_REQ or ALL_REQ Command and transmitted its SENS_RES.

If the Target receives in SENSE State any other valid or invalid command it shall stay in SENSE State.

11.2.1.10 RESOLUTION State

In the RESOLUTION State, the bit frame single device detection method may be applied. Cascade levels are handled inside this State to get the complete NFCID1. See 11.2.1.26.
The Target enters the SELECTED State when it has received a valid SEL_REQ with its complete NFCID1 and sends the SEL_RES back to the initiator.

If the Target receives in the RESOLUTION State any other valid or invalid command it shall fall back to the SENSE State.

11.2.1.11 SELECTED State

In the SELECTED State, the Target shall listen depending on the coding in the SEL_RES to an ATR_REQ command or a valid proprietary command.

The Target shall enter the SLEEP State when a valid SLP_REQ Command is received. In the transport protocol the DSL commands are specified to return the Target to its SLEEP State.

If the Target receives in the SELECTED State any other valid or invalid command it shall fall back to the SENSE State.

11.2.1.12 SLEEP State

In the SLEEP State, the Target shall respond only to an ALL_REQ Command that transits the Target to its RESOLUTION* State.

The Target shall enter the RESOLUTION* State after it has received a valid ALL_REQ Command and transmitted its SENS_RES.

If the Target receives in SLEEP State any other valid or invalid command it shall stay in SLEEP State.

11.2.1.13 RESOLUTION* State

The RESOLUTION* State shall be similar to the RESOLUTION State, a bit frame SDD method can be applied. Cascade levels shall be handled inside this State to get all NFCID1 CLn.

The Target enters the SELECTED* State when it is selected with its complete NFCID1.

If the Target receives in the RESOLUTION* State any other valid or invalid command it shall fall back to the SLEEP State.

11.2.1.14 SELECTED* State

The SELECTED* State shall be similar to the SELECTED State; the Target shall listen depending on the coding in the SEL_RES to an ATR_REQ command or a valid proprietary command. The Target shall enter the SLEEP State when a valid SLP_REQ Command is received.

In the transport protocol the DSL command are specified to return the Target to its SLEEP State.

If the Target receives in the SELECTED* State any other valid or invalid command it shall fall back to the SLEEP State.
11.2.1.15 Command Set

The commands used by the Initiator and the responses by the Targets are described in Table 7.

<table>
<thead>
<tr>
<th>Mnemonic</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>SENS_REQ</td>
<td>Sense Request (sent by Initiator)</td>
</tr>
<tr>
<td>SENS_RES</td>
<td>Sense Response (sent by Target)</td>
</tr>
<tr>
<td>ALL_REQ</td>
<td>Wakeup ALL Request (sent by the Initiator)</td>
</tr>
<tr>
<td>SDD_REQ</td>
<td>Single device detection Request (sent by Initiator)</td>
</tr>
<tr>
<td>SEL_REQ</td>
<td>Select Request (sent by Initiator)</td>
</tr>
<tr>
<td>SEL_RES</td>
<td>Select Response (sent by Target)</td>
</tr>
<tr>
<td>SLP_REQ</td>
<td>Sleep Request (sent by Initiator)</td>
</tr>
</tbody>
</table>

11.2.1.16 SENS_REQ and ALL_REQ Command

The SENS_REQ and ALL_REQ Commands shall be sent by the Initiator to probe the field for Targets. They shall be transmitted within a Short Frame.

Particularly the ALL_REQ Command shall be sent by the Initiator to put Targets which have entered the SLEEP State back into the RESOLUTION* State. They shall then participate in further SDD procedures. Table 8 shows the coding of SENS_REQ and ALL_REQ Commands, which use the Short frame format.

<table>
<thead>
<tr>
<th>Short Frame Commands</th>
<th>b6</th>
<th>b5</th>
<th>b4</th>
<th>b3</th>
<th>b2</th>
<th>b1</th>
<th>b0</th>
</tr>
</thead>
<tbody>
<tr>
<td>SENS_REQ</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>ALL_REQ</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>Proprietary</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
</tr>
<tr>
<td>Proprietary</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>x</td>
<td>x</td>
<td>x</td>
</tr>
<tr>
<td>RFU</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

11.2.1.17 SENS_RES

After an SENS_REQ command is transmitted by the Initiator, all Targets in the SENSE State shall respond synchronously with SENS_RES.

After an ALL_REQ command is transmitted by the Initiator, all Targets in the SENSE or SLEEP State shall respond synchronously with SENS_RES.

The Initiator shall detect any collision that may occur when multiple Targets respond.

11.2.1.18 Coding of SENS_RES

 Coding for bit frame single device detection is specified as follows:

<table>
<thead>
<tr>
<th>bit 15</th>
<th>bit 14</th>
<th>bit 13</th>
<th>bit 12</th>
<th>bit 11</th>
<th>bit 10</th>
<th>bit 9</th>
<th>bit 8</th>
<th>bit 7</th>
<th>bit 6</th>
<th>bit 5</th>
<th>bit 4</th>
<th>bit 3</th>
<th>bit 2</th>
<th>bit 1</th>
<th>bit 0</th>
</tr>
</thead>
<tbody>
<tr>
<td>ZERO</td>
<td>ZERO</td>
<td>ZERO</td>
<td>ZERO</td>
<td>Proprietary coding</td>
<td>NFCID1 size bit frame</td>
<td>ZERO</td>
<td>Bit frame single device detection</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
− bits b15 to b12 shall be set to ZERO.
− bit b11 to b8 indicate additional and proprietary coding and shall be ignored on interchange.
− bits b7 and b6 code the NFCID1 size (single, double or triple). See Table 10.

Table 10 — Coding of bit 7, bit 6 for bit frame single device detection

<table>
<thead>
<tr>
<th>bit 7</th>
<th>bit 6</th>
<th>Explanation</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>NFCID1 size: single</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>NFCID1 size: double</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>NFCID1 size: triple</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>RFU</td>
</tr>
</tbody>
</table>

− bit 5 shall be set to ZERO
− One out of the five bits b0, b1, b2, b3 or b4 shall be set to ONE to indicate bit frame SDD. See Table 11.

Table 11 — Coding of b0 to b4 for bit frame SDD

<table>
<thead>
<tr>
<th>b4</th>
<th>b3</th>
<th>b2</th>
<th>b1</th>
<th>b0</th>
<th>Meaning</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>bit frame SDD</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>bit frame SDD</td>
</tr>
<tr>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>bit frame SDD</td>
</tr>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>bit frame SDD</td>
</tr>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>bit frame SDD</td>
</tr>
</tbody>
</table>

11.2.1.19 SDD_REQ and SEL_REQ Command

These commands shall be used during an SDD. The SDD_REQ and SEL_REQ Commands consist of:
− SEL_CMD (1 byte code; See Table 12.)
− SEL_PAR (1 byte code; See Table 13 and Table 14.)
− 0 to 40 data bits of NFCID1 \( CL_n \) according to the value of SEL_PAR.

SEL_CMD specifies the cascade level \( CL_n \).

The SDD_REQ command shall be transmitted within the bit oriented SDD Frame.

The SEL_REQ command shall be transmitted within the Standard Frame.

As long as SEL_PAR does not specify 40 valid bits, the command shall be called SDD_REQ Command, where the Target remains in RESOLUTION or RESOLUTION* State.

If SEL_PAR specifies 40 data bits of NFCID1 \( CL_n \) (SEL_PAR = 70), a CRC shall be appended. This command shall be called SEL_REQ command.

If the Target has transmitted the complete NFCID1, it transits from RESOLUTION State to SELECTED State or from RESOLUTION* State to SELECTED* State and indicates in its SEL_RES response that NFCID1 is complete.

Otherwise, the Target remains in RESOLUTION or RESOLUTION* State and the Initiator shall initiate a new SDD with increased cascade level.
11.2.1.20 Coding of SEL_CMD

Table 12 defines the coding of the SEL_CMD byte.

Table 12 — Coding of SEL_CMD

<table>
<thead>
<tr>
<th>bit 7</th>
<th>bit 6</th>
<th>bit 5</th>
<th>bit 4</th>
<th>bit 3</th>
<th>bit 2</th>
<th>bit 1</th>
<th>bit 0</th>
<th>Hex notation</th>
<th>Meaning</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>(93)</td>
<td>Select cascade level 1</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>(95)</td>
<td>Select cascade level 2</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>(97)</td>
<td>Select cascade level 3</td>
</tr>
</tbody>
</table>

Other settings are RFU

11.2.1.21 Coding of SEL_PAR

The length of the SEL_PAR shall be 1 byte. The upper 4 bits shall be called “Byte count” and shall specify the integer part of the number of all valid data bits transmitted by the Initiator (including SEL_CMD and SEL_PAR) divided by 8. Consequently, the minimum value of “Byte count” shall be 2 and the maximum value shall be 7.

Table 13 — Coding of SEL_PAR upper 4 bits

<table>
<thead>
<tr>
<th>bit 7</th>
<th>bit 6</th>
<th>bit 5</th>
<th>bit 4</th>
<th>Meaning</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>Byte count = 2</td>
</tr>
<tr>
<td>0</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>Byte count = 3</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>Byte count = 4</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>Byte count = 5</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>Byte count = 6</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>Byte count = 7</td>
</tr>
</tbody>
</table>

The lower 4 bits shall be called “bit count” and specify the number of all valid data bits transmitted by the Initiator (including SEL_CMD and SEL_PAR) modulo 8.

Table 14 — Coding of SEL_PAR, lower 4 bits

<table>
<thead>
<tr>
<th>bit 3</th>
<th>bit 2</th>
<th>bit 1</th>
<th>bit 0</th>
<th>Meaning</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>bit count = 0</td>
</tr>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>bit count = 1</td>
</tr>
<tr>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>bit count = 2</td>
</tr>
<tr>
<td>0</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>bit count = 3</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>bit count = 4</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>bit count = 5</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>bit count = 6</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>bit count = 7</td>
</tr>
</tbody>
</table>

11.2.1.22 Coding of SEL_RES

The SEL_RES shall be transmitted by the Target when SEL_PAR has specified 40 valid data bits and when all these data bits match with NFCID1 CL<n>. The SEL_RES byte shall be followed by 2 bytes CRC according to A.1.
The coding of bit 2 (cascade bit) and b6 is given in Table 15. The coding of bit 6 in SEL shall indicate the NFCIP-1 Target device Attribute Request ability.

Table 15 — Coding of SEL_RES

<table>
<thead>
<tr>
<th>bit 7</th>
<th>bit 6</th>
<th>bit 5</th>
<th>bit 4</th>
<th>bit 3</th>
<th>bit 2</th>
<th>bit 1</th>
<th>bit 0</th>
<th>Meaning</th>
</tr>
</thead>
<tbody>
<tr>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>1</td>
<td>x</td>
<td>x</td>
<td>Cascade bit set: NFCID1 not complete.</td>
</tr>
<tr>
<td>x</td>
<td>1</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>0</td>
<td>x</td>
<td>x</td>
<td>NFCID1 complete, Target compliant with NFC transport protocol. Request for Attributes supported.</td>
</tr>
<tr>
<td>x</td>
<td>0</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>0</td>
<td>x</td>
<td>x</td>
<td>NFCID1 complete, Target not compliant with transport protocol, Request for Attributes not supported.</td>
</tr>
</tbody>
</table>

11.2.1.23 Select sequence

The purpose of the select sequence is to get the NFCID1 from one Target and to select this Target for further communication.
Figure 13 — Initialization and SDD Flow Chart for Initiator
11.2.1.25 SDD loop within each cascade level

The following algorithm shall apply to the SDD loop: If the NFCID1 of a Target is complete, the Initiator may skip from step 2 to step 10 selecting this Target without performing the SDD loop.

This section describes the bit collision detection protocol applicable for NFCIP-1 device communication at 106 kbps in the Passive communication mode.

The following steps define SDD Flow:

1. The Initiator shall assign SEL_CMD with the code for the selected SDD type and cascade level.

2. The Initiator assigns SEL_PAR with the value of (20). This value shall define that the Initiator will transmit no part of NFCID1 CL\_n. Consequently these command forces all Targets in the field to respond with their complete NFCID1 CL\_n.

3. The Initiator shall transmit SEL_CMD and SEL_PAR.

4. All Targets in the field shall respond with their complete NFCID1 CL\_n.

5. Assuming the Targets in the field have a different NFCID1, then if more than one-Target responds, a collision occurs. If no collision occurs, steps 6 to 10 are skipped.

6. The Initiator shall recognise the position of the first collision.

7. The Initiator assigns SEL_PAR with a value that specifies the number of valid bits of NFCID1 CL\_n. The valid bits shall be part of the NFCID1 CL\_n that was received before a collision occurred followed by a ZERO or ONE, decided by the Initiator. A typical implementation adds a ONE.

8. The Initiator shall transmit SEL_CMD and SEL_PAR, followed by the valid bits.

9. Only Targets of which the part of NFCID1 CL\_n is equal to the valid bits transmitted by the Initiator shall transmit their remaining bits of the NFCID1 CL\_n.

10. If further collisions occur, steps 6 to 9 shall be repeated. The maximum number of loops will be 32.

11. If no further collision occurs, the Initiator assigns SEL_PAR with the value of (70). This value shall define that the Initiator will transmit the complete NFCID1 CL\_n.

12. The Initiator transmits SEL_CMD and SEL_PAR, followed by all 40 bits of NFCID1 CL\_n, followed by CRC as described in A.1.

13. The Target which NFCID1 CL\_n shall match the 40 bits responds with its SEL_RES.

14. If the NFCID1 is complete, the Target shall transmit SEL_RES with cleared cascade bit and transit from RESOLUTION State to SELECTED State or from RESOLUTION* State to SELECTED* State.

15. The Initiator shall check if the cascade bit of SEL_RES is set to decide whether further SDD loop with increased cascade level shall follow.

11.2.1.26 NFCID1 contents and cascade levels

The NFCID1 shall consist of 4, 7 or 10 NFCID1 bytes. Consequently, the Target shall handle up to 3 cascade levels to get all NFCID1 bytes. Within each cascade level, a part of NFCID1 shall be transmitted to the Initiator. According to the cascade level, three types of NFCID1 size are defined. This NFCID1 size has to be consistent with Table 16.
### Table 16 — Size of NFCID1

<table>
<thead>
<tr>
<th>NFCID1 size</th>
<th>Cascade level</th>
<th>Number of NFCID1 bytes</th>
</tr>
</thead>
<tbody>
<tr>
<td>single</td>
<td>1</td>
<td>4</td>
</tr>
<tr>
<td>double</td>
<td>2</td>
<td>7</td>
</tr>
<tr>
<td>triple</td>
<td>3</td>
<td>10</td>
</tr>
</tbody>
</table>

The NFCID1 shall be a random number that is dynamically generated by the Target. The first byte (nfcid10) of the NFCID1 shall assign the content of the following bytes of the NFCID1. See Table 17.

### Table 17 — Single size NFCID1s

<table>
<thead>
<tr>
<th>nfcid10</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>(08)</td>
<td>nfcid11 to nfcid13 is a random number which shall be dynamically generated</td>
</tr>
<tr>
<td>(x0) to (x7)</td>
<td>proprietary fixed number</td>
</tr>
<tr>
<td>(x9) to (xE)</td>
<td>Set to all ZEROs.</td>
</tr>
</tbody>
</table>

The value (88) of the cascade tag CT shall not be used for nfcid10 in single size NFCID1.

The first byte of the NFCID1 describes the CT in case of double size NFCID1, in case of triple size NFCID1 the CT byte describes the first and the 5th byte of the NFCID1. The cascade tag CT shall be set to (88) in case of double and triple size NFCID1.

### Table 18 — Double and triple size NFCID1s

<table>
<thead>
<tr>
<th>nfcid10</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>(01)-(7E)</td>
<td>Proprietary</td>
</tr>
<tr>
<td>All other values</td>
<td>RFU</td>
</tr>
</tbody>
</table>

In case of double or triple NFCID1s the nfcid10 to nfcid16 or nfcid10 to nfcid19 shall be a random number that is dynamically generated by the Target.
The purpose of the cascade tag shall be to force a collision with Targets that have a smaller NFCID1 size. The following algorithm shall apply to the Initiator to get the complete NFCID1:

- The Initiator shall select cascade level 1.
- The SDD shall be performed.
- The Initiator shall check the cascade bit of SEL_RES.
- If the cascade bit is set, the Initiator shall increase the cascade level and initiate a new single device detection.

11.2.1.27 SLP_REQ Command

The SLP_REQ Command consists of two bytes followed by CRC and shall be transmitted within Standard Frame. The CRC is described in A.1.

If the Target responds with any modulation during a period of 1 ms after the end of the frame containing the SLP_REQ command, this response shall be interpreted as ‘not acknowledge’.

11.2.2 Initialisation and SDD at 212 kbps and 424 kbps

11.2.2.1 Start and end of communication

The start of the Passive communication shall be signalled by the presence of modulation of the carrier frequency. The communication shall start with the preamble sequence of minimum 48 bits with all logical ‘ZERO’ encoded. The end of communication shall be forecasted from the Length field of the frame.
Figure 16 — Start and end of communication

After one NFCIP-1 device has finished communication, the other shall delay for a period of at least $8 \times 64/fc$ before starting transmission by sending the preamble sequence as shown in Figure 17.

Figure 17 — Delay between frames

11.2.2.2 Frame format
The frame format of shall consist of Preamble, SYNC, Length, Payload, and CRC. The Preamble shall be 48 bits minimum all logical ZEROs.
The SYNC shall be 2 bytes. The 1st byte of the SYNC shall be (B2) and the 2nd byte shall be (4D).

<table>
<thead>
<tr>
<th>Preamble</th>
<th>SYNC</th>
<th>Length</th>
<th>Payload</th>
<th>CRC</th>
</tr>
</thead>
</table>

Figure 18 — Frame format

The Length shall be an 8-bit field and it shall be set to the number of bytes to be transmitted in Payload plus 1. The range of the Length shall be 2 to 255, and other settings are RFU.
The Payload shall consist $n$ 8-bit-bytes of data where $n$ is indicated by the number of data bytes.
The CRC shall be calculated according to A.3.

11.2.2.3 Single Device Detection at 212 kbps and 424 kbps
The basic technique of the SDD procedure shall be the Time Slot method. The number of the Slot shall be the integer value beyond zero. The Initiator shall send Polling Requests. The Target shall respond at random in each controllable Time Slot. The Initiator shall be able to read NFCID2 data (see 11.2.2.4) of Target(s) in different Time Slots.
After obtaining NFCID2 data from Target(s) in the operating field, the Initiator may communicate with multiple Targets.
Up to 16 Time Slots may be supported by agreement between the interchange parties. The number of Time Slot may be indicated by the value TSN in the Polling Request Frame from the Initiator.
A Target, which is already powered up, responds to the Initiator according to the following rules after receiving the Polling Request Frame from the Initiator.
1. The Target shall generate a random number $R$ in the range 0 to TSN.
2. The Target shall wait until the Time Slot is matched to R, then send the Polling Response Frame and wait for the next Request. The Target may ignore a Polling Request to reduce instances of collision of Responses.

The communication between the Initiator and the Target shall be initiated as follows:

1. The Target gets power from the operating field generated by the Initiator.

2. The Target shall become ready for receiving a Polling Request from the Initiator in maximum 2 seconds from power up.

3. The Target shall wait for a Polling Request sent from the Initiator. The Initiator may send a Polling Request without waiting for the Target to become ready.

4. If the Initiator fails to receive Polling Response, then the Initiator may send Polling Request again. The Initiator of the Passive communication mode shall keep RF power on while executing the SDD procedure.

The delay $T_d$ between the end of the Request Frame and the first Time Slot shall be $512 \times \frac{64}{fc}$.

The Time Slot unit $T_s$ shall be $256 \times \frac{64}{fc}$.

Figure 19 illustrates an example situation of the SDD by Time Slot. In this example, 5 Targets are responding. The Initiator may be able to get the Response information of the Target 2, 4, and 5 excluding 1 and 3. Because a collision has occurred at the Time Slot 1.

The Initiator may repeat SDD procedure.

<table>
<thead>
<tr>
<th>Time→</th>
<th>$T_d$</th>
<th>$T_s$</th>
<th>$T_s$</th>
<th>$T_s$</th>
<th>$T_s$</th>
</tr>
</thead>
<tbody>
<tr>
<td>REQ from Initiator</td>
<td>Time Slot 0</td>
<td>Time Slot 1</td>
<td>Time Slot 2</td>
<td>Time Slot 3</td>
<td></td>
</tr>
<tr>
<td>RES from Target 4</td>
<td>RES from Target 3</td>
<td>RES from Target 1</td>
<td>RES from Target 5</td>
<td>RES from Target 2</td>
<td></td>
</tr>
</tbody>
</table>

![Figure 19 — Single Device Detection by Time Slot](image)

### 11.2.2.4 NFCID2 contents

NFCID2 shall be an 8-byte number for identifying NFCIP-1 devices. The 2-byte prefix code shall be followed by a 6-byte number in the NFCID2. The prefix code shall define the characteristics for the 6-byte number.

The 6-byte number shall be randomly generated while the prefix code is (01) (FE). Other settings for the prefix code are RFU.

### 11.2.2.5 Polling Request Frame format

An Initiator for finding Targets shall send polling frame.

<table>
<thead>
<tr>
<th>Preamble (48 bit min.)</th>
<th>SYNC (16 bit)</th>
<th>Length (8 bit)</th>
<th>Payload</th>
<th>CRC (16 bit)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td>(00)</td>
<td>(FF)</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>(FF)</td>
<td>(00) TSN</td>
</tr>
</tbody>
</table>

![Figure 20 — Polling Request Frame format](image)

The Preamble shall be 48 bits minimum all logical ZEROs.
The synchronisation (SYNC) pattern shall be 2 byte. The 1st byte of the synchronisation pattern shall be (B2) and the 2nd byte shall be (4D).

The Length shall be set to (06).

The 1st byte of the Payload shall be set to (00).

The 2nd byte and the 3rd of Payload shall be set to (FF) and other settings are RFU.

The 4th byte of Payload shall be set to (00), and other settings are RFU.

The TSN shall be (00), (01), (03), (07), or (0F). Any other settings are RFU.

The CRC shall be calculated according to A.3.

Figure 19 illustrates an example where the TSN is (03). If the TSN is set to (00) then only the Time Slot 0 shall be used.

11.2.2.6 Polling Response Frame format

Target shall send the following frame as the Polling Response toward the Polling Request.

<table>
<thead>
<tr>
<th>Preamble</th>
<th>SYNC</th>
<th>Length</th>
<th>Payload</th>
<th>CRC</th>
</tr>
</thead>
<tbody>
<tr>
<td>(48 bit )</td>
<td>(16 bit)</td>
<td>(8 bit)</td>
<td>NFCID2</td>
<td>(16 bit)</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>Pad</td>
<td></td>
</tr>
</tbody>
</table>

The Preamble shall be 48 bits minimum all logical “ZERO”.

The synchronisation (SYNC) pattern shall be 2 byte. The 1st byte of the synchronisation pattern shall be (B2) and the 2nd byte shall be (4D).

The Length field shall be set (12).

The start byte of the Payload shall be set to (01). The Payload shall contain 8-byte of NFCID2 and 8-byte of Pad. The Pad shall be ignored for data interchange.

The CRC shall be calculated according to A.3.

11.3 Active communication mode

11.3.1 Initialisation at 106, 212, and 424 kbps

The application switches to Initiator for the Active communication mode and may choose 106 kbps, 212 kbps or 424 kbps.

11.3.2 Active communication mode RF Collision Avoidance

The RF Collision Avoidance shall be executed according to the following timing chart. See Figure 22.

− The Initiator shall perform the initial RF Collision Avoidance.
− The first command sends by the Initiator is the ATR_REQ in the Active communication mode at a selected transfer speed.
− The Initiator shall switch off the RF field.
− The Target performs the response RF Collision Avoidance.
− The Target sends the ATR_RES as a response to the ATR_REQ in the same transfer speed as it has received the ATR_REQ and switch of the RF field.
− The Initiator performs the response RF Collision Avoidance with Time Jitters indicator \( n = 0 \).
− The Initiator sends the PSL_REQ in order to change parameter or sends the DEP_REQ to start the data exchange protocol.
11.3.2.1 Collision Avoidance for Active communication mode

In case where 2 Targets or more are in the field the one with the lowest \( n \) will answer first and the other will not answer.

In case of 2 or more Targets answering in exactly the same Time Period, the Initiator will detect a collision and it will re-send the ATR_REQ, which is described in 12.5.1.1.

After the first Target response is detected by the Initiator the Time Jitters parameter indicator \( n \) shall be set to 0 for further communication.

12 Transport Protocol

The transport protocol shall be handled in three parts

- Activation of the protocol, which includes the Request for Attributes and the Parameter Selection.
- The data exchange protocol, and
- The deactivation of the protocol including the Deselect and the Release.

12.1 Transport Data

User data shall be transported by the Transport Data field in the Frame format. Figure 23 shows the position of the Transport Data field in each Frame format.

The structure for the Frame format for 106 kbps is shown in clause 11.2.1.5.2. The start byte SB shall be set to (F0). The LEN byte shall be set to the length of the Transport Data field plus 1. The range of the LEN shall be in the range of 3 to 255. The E1 is the CRC for the Frame format of 106 kbps as described in A.1. Other settings of LEN are prohibited by this Standard.

The structure for the Frame format for 212 kbps and 424 kbps as shown in the clause 11.2.2.2 describing the Preamble PA and the Synchronous pattern bytes SYNC.

The LEN byte shall be set to the length of the Transport Data field plus 1. The value of LEN shall be in the range of 3 to 255. The E2 is the CRC for the Frame format of 212 kbps and 424 kbps as described in A.3. Other settings of LEN are prohibited by this Standard.
The Transport data field contains the mandatory command bytes CMD0 and CMD1 as described in clause 12.4 and the data bytes Byte 0 to Byte \( n \). The content of Byte 0 to Byte \( n \) depends on the command byte CMD1 and may contain information. In that case they are mandatory. Data bytes are optional.

---

**Figure 23 — Transport Frame format Definition**

### 12.2 Passive communication mode Activation flow

The following activation sequence shall be applied:

1. The Initiator shall perform the initial RF Collision Avoidance sequence as defined in clause 11.1.1.
2. The Initiator shall perform the Initialisation and SDD for the Passive communication mode at a chosen transfer speed as defined in clauses 11.2.
3. The support of the NFCIP-1 protocol shall be checked at the different transfer speeds according to the Attribute Request as described in clause 12.5.1.1.
4. The Target may fall back to the Initialisation and SDD if no ATR_REQ is supported.
5. The ATR_REQ may be send by the Initiator as a next command after receiving the Attribute Request is available.
6. The Target shall send its ATR_RES as answer to the ATR_REQ. The Target shall only answer to the ATR_REQ if the ATR_REQ is received directly after selection.
7. If the Target supports any changeable parameter in the ATR_REQ, a PSL_REQ may be used by the Initiator as the next command after receiving the ATR_REQ to change parameters.
8. The Target shall send a PSL_RES as answer to the PSL_REQ.
9. A Target does not need to complement the parameter selection, if it does not support any changeable parameters in the ATR_RES.
10. The transparent data shall be sent using the data exchange transport protocol.

The Initiator activation sequence for a Target in the Passive communication mode is shown in Figure 24.
Start

Initial RFCA

Switch to passive NFC

Initialisation and SDD loop

Request Attribute?

no

Proprietary protocol

yes

Send ATR_REQ

Receive ATR_RES

Parameter change?

yes

Send PSL_REQ

Receive PSL_RES

Change Parameter

Data Exchange Protocol (DEP)

no

Receive DSL_RES

Send DSL_REQ

Receive RLS_RES

Send RLS_REQ

Initilisation and anti-collision

Protocol Activation and Parameter Selection

Figure 24 — Activation protocol in Passive communication mode
12.3 **Active communication mode Activation flow**

The following activation sequence for the protocol in the Active communication mode shall be applied:

1. The Initiator shall perform the initial RF Collision Avoidance sequence as defined in clause 11.1.1.

2. The Initiator shall switch to the Active communication mode and select the transfer speed.

3. The Initiator shall send the ATR_REQ.

4. The Target shall send its ATR_RES in response to the ATR_REQ. After a successful response the device is selected.

5. If the Initiator detects a collision of data the ATR_REQ shall be re-sent.

6. If the Target supports any changeable parameter in the ATR_RES, a PSL_REQ may be used by the Initiator as the next command after receiving the ATR_RES to change parameters.

7. The Target shall send a PSL_RES in response to the PSL_REQ.

8. A Target does not need to complement the parameter selection, if it does not support any changeable parameters in the ATR_RES.

The Initiator activation sequence for a Target in the Active communication mode is shown in Figure 25.
Start

Initial RFC A

Switch to Active NFC

Send ATR_REQ

Receive ATR_RES

Parameter change ?

yes

Receive WUP_RES

Send WUP_REQ

Receive PSL_RES

Send PSL_REQ

Receive DSL_RES

Send DSL_REQ

no

Change Parameter

Data Exchange Protocol (DEP)

Figure 25 — Activation Protocol in Active communication mode
12.4 Commands

The Command Bytes shall consist of 2 bytes. The first byte shall be CMD0 and the second byte shall be CMD1. The code of the Command Bytes shall specify the Request and Response according to the Table 19.

Table 19 — NFCIP-1 Protocol Command Set

<table>
<thead>
<tr>
<th>Mnemonic</th>
<th>Command Bytes</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>ATR_REQ</td>
<td>(D4) (00)</td>
<td>Attribute Request (sent by Initiator)</td>
</tr>
<tr>
<td>ATR_RES</td>
<td>(D5) (01)</td>
<td>Attribute Response (sent by Target)</td>
</tr>
<tr>
<td>WUP_REQ</td>
<td>(D4) (02)</td>
<td>Wakeup Request (sent by Initiator in Active mode only)</td>
</tr>
<tr>
<td>WUP_RES</td>
<td>(D5) (03)</td>
<td>Wakeup Response (sent by Target in Active mode only)</td>
</tr>
<tr>
<td>PSL_REQ</td>
<td>(D4) (04)</td>
<td>Parameter selection Request (sent by Initiator)</td>
</tr>
<tr>
<td>PSL_RES</td>
<td>(D5) (05)</td>
<td>Parameter selection Response (sent by Target)</td>
</tr>
<tr>
<td>DEP_REQ</td>
<td>(D4) (06)</td>
<td>Data Exchange Protocol Request (sent by Initiator)</td>
</tr>
<tr>
<td>DEP_RES</td>
<td>(D5) (07)</td>
<td>Data Exchange Protocol Response (sent by Target)</td>
</tr>
<tr>
<td>DSL_REQ</td>
<td>(D4) (08)</td>
<td>Deselect Request (sent by Initiator)</td>
</tr>
<tr>
<td>DSL_RES</td>
<td>(D5) (09)</td>
<td>Deselect Response (sent by Target)</td>
</tr>
<tr>
<td>RLS_REQ</td>
<td>(D4) (0A)</td>
<td>Release Request (sent by Initiator)</td>
</tr>
<tr>
<td>RLS_RES</td>
<td>(D5) (0B)</td>
<td>Release Response (sent by Target)</td>
</tr>
</tbody>
</table>

12.5 Activation of the protocol

12.5.1 Attribute Request and Response Commands

12.5.1.1 Attribute Request (ATR_REQ)

This clause defines the Attribute Request ATR_REQ with all its parameter bytes. The Initiator shall send the ATR_REQ to the selected Target.

- **CMD 0**: Shall be set to (D4).
- **CMD 1**: ATR_REQ
- **Byte 0 to Byte 9**: NFCID3i0 to NFCID3i9
- **Byte 10**: DIDi
- **Byte 11**: BSi
- **Byte 12**: BRi
- **Byte 13**: PPi
- **Byte 14 to Byte n**: [Gi[0]] to [Gi[n]]

Figure 26 — Structure of the ATR_REQ

12.5.1.1.1 Definition of the ATR_REQ bytes

**CMD 0**: Shall be set to (D4).

**CMD 1**: ATR_REQ

The ATR_REQ byte shall specify the Attribute Request for the Initiator. The value of ATR_REQ shall be set to (00).

**Byte 0 to Byte 9**: NFCID3i

The 10 NFCID3i bytes define the random identifier NFCID3i of the Initiator. NFCID3 shall be an ID dynamically generated by the application and be fixed during one communication. In Passive communication mode at 212 and 424 kbps, NFCID2t shall replace NFCID3i.

**Byte 10**: DIDi

The DID byte shall be used for multiple data transport protocol activation with more than one Target. The range of the DIDi shall be defined between 1 and 14. The value ZERO shall be used, if no DID is used during the data transport protocol. All other values are prohibited by this Standard.
Byte 11: BSi
The BSi byte shall specify the send-bit rates supported by the Initiator device. The coding of bits is as follows:

<table>
<thead>
<tr>
<th>bit 7</th>
<th>bit 6</th>
<th>bit 5</th>
<th>bit 4</th>
<th>bit 3</th>
<th>bit 2</th>
<th>bit 1</th>
<th>bit 0</th>
</tr>
</thead>
<tbody>
<tr>
<td>ZERO</td>
<td>ZERO</td>
<td>ZERO</td>
<td>ZERO</td>
<td>DSi</td>
<td>DSi</td>
<td>DSi</td>
<td>DSi</td>
</tr>
</tbody>
</table>

*Figure 27 — Coding of the BSi byte*

- bit 7 to bit 4: shall be set to ZERO, all other values are RFU.
- bit 3: DSi = 64 supported, if bit is set to ONE
- bit 2: DSi = 32 supported, if bit is set to ONE
- bit 1: DSi = 16 supported, if bit is set to ONE
- bit 0: DSi = 8 supported, if bit is set to ONE

Byte 12: BRi
The BRi byte shall specify the receive-bit rates supported by the Initiator device. The coding of bits is as follows:

<table>
<thead>
<tr>
<th>bit 7</th>
<th>bit 6</th>
<th>bit 5</th>
<th>bit 4</th>
<th>bit 3</th>
<th>bit 2</th>
<th>bit 1</th>
<th>bit 0</th>
</tr>
</thead>
<tbody>
<tr>
<td>ZERO</td>
<td>ZERO</td>
<td>ZERO</td>
<td>ZERO</td>
<td>DRi</td>
<td>DRi</td>
<td>DRi</td>
<td>DRi</td>
</tr>
</tbody>
</table>

*Figure 28 — Coding of the BRi byte*

- bit 7 to bit 4: shall be set to ZERO, all other values are RFU.
- bit 3: DRi = 64 supported, if bit is set to ONE
- bit 2: DRi = 32 supported, if bit is set to ONE
- bit 1: DRi = 16 supported, if bit is set to ONE
- bit 0: DRi = 8 supported, if bit is set to ONE

Byte 13: PPi
The PPi byte specifies optional parameters used by Initiator device. The coding of bits shall be as follows:

<table>
<thead>
<tr>
<th>bit 7</th>
<th>bit 6</th>
<th>bit 5</th>
<th>bit 4</th>
<th>bit 3</th>
<th>bit 2</th>
<th>bit 1</th>
<th>bit 0</th>
</tr>
</thead>
<tbody>
<tr>
<td>ZERO</td>
<td>ZERO</td>
<td>Lri</td>
<td>Lri</td>
<td>ZERO</td>
<td>ZERO</td>
<td>Gi</td>
<td>NAD</td>
</tr>
</tbody>
</table>

*Figure 29 — Coding of the PPi byte*

- bit 7 and bit 6: Shall be set to ZERO.
- bit 5 and bit 4: Length Reduction value.

Table 20 — Definition of Lri

<table>
<thead>
<tr>
<th>Lri</th>
<th>LENMAX</th>
</tr>
</thead>
<tbody>
<tr>
<td>00</td>
<td>Only Byte 0 to Byte 63 is valid in the Transport Data</td>
</tr>
<tr>
<td>01</td>
<td>Only Byte 0 to Byte 127 is valid in the Transport Data</td>
</tr>
<tr>
<td>10</td>
<td>Only Byte 0 to Byte 191 is valid in the Transport Data</td>
</tr>
<tr>
<td>11</td>
<td>Only Byte 0 to Byte 255 is valid in the Transport Data</td>
</tr>
</tbody>
</table>
- bit 3 and bit 2: Shall be set to ZERO.
- bit 1: If bit is set to ONE then it indicates General bytes are available.
- bit 0: If bit is set to ONE then it indicates the Initiator uses NAD.

**Byte 14 to Byte n**: Gi[0] to Gi[n]

The general bytes shall be optional and designate general information. The maximum length of the ATR_REQ subtracted by the mandatory bytes give the maximum number of general bytes.

**12.5.1.2 Attribute Response (ATR_RES)**

This clause defines the answer to request for attributes. The ATR_RES shall be the response to the ATR_REQ and shall be sent by the selected NFCIP-1 Target device.

<table>
<thead>
<tr>
<th>CMD 0</th>
<th>CMD 1</th>
<th>Byte 0</th>
<th>...</th>
<th>Byte 9</th>
<th>Byte 10</th>
<th>Byte 11</th>
<th>Byte 12</th>
<th>Byte 13</th>
<th>Byte 14</th>
<th>Byte 15</th>
<th>...</th>
<th>Byte n</th>
</tr>
</thead>
<tbody>
<tr>
<td>(D5)</td>
<td>(01)</td>
<td>nfcid3t0</td>
<td>...</td>
<td>nfcid3t9</td>
<td>DIDt</td>
<td>BST</td>
<td>BRt</td>
<td>TO</td>
<td>Ptt</td>
<td>[Gt0]</td>
<td>...</td>
<td>[Gt[n]]</td>
</tr>
</tbody>
</table>

*Figure 30 — Structures of the ATR_RES*

**12.5.1.2.1 Definition of the ATR_RES bytes**

**CMD 0**: Shall be set to (D5).

**CMD 1**: ATR_RES

The ATR_RES byte shall specify the Target’s Response to the ATR_REQ send by the Initiator. The value of CMD1 for ATR_RES shall be set to (01).

**Byte 0 to Byte 9**: NFCID3t

The 10 nfcid3t bytes define the random identifier NCID3t of the Target. NFCID3 should be an ID generated by the application. The content of NFCID3 may be the same as NFCID1 or NFCID2.

**Byte 10**: DIDt

The DID byte shall be used for multiple data transport protocol activation with more than one Target. The DIDt shall have the same value as the DIDi. All other values are prohibited by this Standard. For usage of DIDt see 12.5.1.1.1.

**Byte 11**: BST

The BST byte shall specify the supported transfer speed of the Target device. The coding of bits is defined as follows:

<table>
<thead>
<tr>
<th>bit 7</th>
<th>bit 6</th>
<th>bit 5</th>
<th>bit 4</th>
<th>bit 3</th>
<th>bit 2</th>
<th>bit 1</th>
<th>bit 0</th>
</tr>
</thead>
<tbody>
<tr>
<td>ZERO</td>
<td>ZERO</td>
<td>ZERO</td>
<td>ZERO</td>
<td>DST</td>
<td>DST</td>
<td>DST</td>
<td>DST</td>
</tr>
</tbody>
</table>

*Figure 31 — Coding of the BST byte*

- bit 7 to bit 4: Shall be set to ZERO.
- bit 3: DST = 64 supported, if bit is set to ONE.
- bit 2: DST = 32 supported, if bit is set to ONE.
- bit 1: DST = 16 supported, if bit is set to ONE.
- bit 0: DST = 8 supported, if bit is set to ONE.

**Byte 12**: BRt

The BRt byte shall specify the supported receive bit rates of the Target device. The coding of bits is defined as follows:
Figure 32 — Coding of the BRt byte

- bit 7 to bit 4: Shall be set to ZERO.
- bit 3: DRt = 64 supported, if bit is set to ONE.
- bit 2: DRt = 32 supported, if bit is set to ONE.
- bit 1: DRt = 16 supported, if bit is set to ONE.
- bit 0: DRt = 8 supported, if bit is set to ONE.

Byte 13: TO

The TO byte shall specify the timeout value of the Target NFCIP-1 device for the data transport protocol. The timeout calculation shall start with the last bit send by the Initiator and stop with the first bit send by the Target. The timeout is specified as follows:

Figure 33 — Coding of the TO byte

- bit 7 to bit 4: Shall be set to all ZEROs.
- bit 3 to bit 0: WT: Waiting Time.

The Response Waiting Time (RWT) shall be calculated by the following formula:

\[ RWT = (256 \times 16 / fc) \times 2WT \]

Where the value of WT shall be the range from 0 to 14 and the value of 15 is RFU. The default value of WT shall be 14.

For WT = 0, RWT = RWTMIN (302 µs)
For WT = 14, RWT = RWTMAX (4 949 ms)

Byte 14: PPt

The PPt byte specifies optional parameters used by Target device. The coding of bits shall be as follows:

Figure 34 — Coding of the PPt byte

- bit 7 and bit 6: Shall be set to ZERO.
- bit 5 and bit 4: Length Reduction value.

Table 21 — Definition of LRt

<table>
<thead>
<tr>
<th>LRt</th>
<th>LENMAX</th>
</tr>
</thead>
<tbody>
<tr>
<td>00</td>
<td>Only Byte 0 to Byte 63 is valid in the Transport Data</td>
</tr>
<tr>
<td>01</td>
<td>Only Byte 0 to Byte 127 is valid in the Transport Data</td>
</tr>
<tr>
<td>10</td>
<td>Only Byte 0 to Byte 191 is valid in the Transport Data</td>
</tr>
<tr>
<td>11</td>
<td>Only Byte 0 to Byte 255 is valid in the Transport Data</td>
</tr>
</tbody>
</table>
- bit 3 and bit 2: Shall be set to ZERO.
- bit 1: If bit is set to ONE then it indicates General bytes available.
- bit 0: If bit is set to ONE then it indicates the Target uses NAD.

**Byte 14 to Byte n: Gt[0] to Gt[n]**
The Gt bytes shall be optional and designate general information. The maximum length of the ATR_RES subtracted by the mandatory bytes gives the maximum number of general bytes.

### 12.5.1.3 Handling of ATR_REQ and ATR_RES

#### 12.5.1.3.1 Initiator rules
When the Initiator has sent the ATR_REQ and receives a valid ATR_RES the Initiator shall continue with operation.

In any other case the Initiator shall retransmit the ATR_REQ before it shall use the deactivation sequence as defined in clause 12.7. In case of failure of the deactivation sequence it may use the SLP_REQ command as defined in clause 11.2.1.27 for Passive communication mode at 106 kbps.

#### 12.5.1.3.2 Target rules
When the Target has been selected by the last command (for passive mode only) and

- Receives a valid ATR_REQ, the Target
  - shall send its ATR_RES,
  - shall disable to receive a subsequent ATR_REQ.
- Receives any other valid or invalid frame, except a SLP_REQ command for passive communication mode at 106 kbps, the Target
  - shall ignore the block and
  - shall remain in receive mode.

### 12.5.1.4 Handling of timeout TO
Defined by the initially chosen mode, the communication is either active or passive. The handling of the timeout is different for active and passive communication mode.

#### 12.5.1.4.1 Handling in active mode
In active mode the communication flow is handled by switching the carrier frequency.

**Initiator:** The Initiator shall ignore a Target that exceeded RWT calculated using TO byte in ATR_REQ from a Target device and continue operation.

**Target:** The Target shall use a TO value that allows common communication and shall use a supervisory pdu containing a timeout extension to extend the defined RWT. See 12.6.1.1.1.

#### 12.5.1.4.2 Handling of timeout in passive mode
In passive mode the communication is only handled by communication flow. The carrier frequency is not switched.

**Initiator:** The Initiator shall first use error handling and if no response is received, it shall ignore Targets that exceeded the specified timeout and it shall continue communication.

**Target:** The Target shall use a TO value that allows common communication and shall use a supervisory pdu containing a timeout extension to extend the defined RWT. See 12.6.1.1.1.
12.5.1.5 Handling of DID

12.5.1.5.1 Handling of DID in active and in passive mode.

When the Initiator has sent a ATR_REQ containing a DID equal to ZERO and
a) received an ATR_RES containing DID equal to ZERO
   − shall send pdu’s containing no DID to the Target and
   − shall not activate any other Target while this Target is not deactivated.
b) received an ATR_RES containing DID not equal to ZERO
   − shall continue with error handling.

When the Initiator has sent a ATR_REQ containing a DID not equal to ZERO and
a) received an ATR_RES containing the same DID
   − shall send pdu’s containing the DID to the Target and
   − shall not use the DID for any other Targets and
   − shall not use DID=0 for any other Targets.
b) received an ATR_RES containing any other DID
   − shall continue with error handling.

12.5.2 Wakeup Request and Response Commands

The Wakeup Request and Response commands are only defined for the Active communication mode.

12.5.2.1 Wakeup Request (WUP_REQ)

This clause defines the Wakeup Request for Attributes WUP_REQ with its parameter bytes. The Initiator sends the WUP_REQ to the Target only in the Active communication mode. It shall be applied to reactivate a distinct Target device by its NFCID3, which was deactivated by the DSL command.

<table>
<thead>
<tr>
<th>CMD 0</th>
<th>CMD 1</th>
<th>Byte 0</th>
<th>...</th>
<th>Byte 9</th>
<th>Byte 10</th>
</tr>
</thead>
<tbody>
<tr>
<td>(D4)</td>
<td>(02)</td>
<td>nfcid3t0</td>
<td>...</td>
<td>nfcid3t9</td>
<td>DID</td>
</tr>
</tbody>
</table>

Figure 35 — Structure of the WUP_REQ

12.5.2.1.1 Definition of the WUP_REQ bytes

CMD 0: Shall be set to (D4).

CMD 1: WUP_REQ

The WUP_REQ byte shall specify the command Wake Up for the Initiator device. The value of WUP_REQ shall be (02).

Byte 0 to Byte 9: NFCID3t

The 10 nfcid3t bytes shall be defined as the random identifier of the Target. For the WUP_REQ command the Initiator shall send the known NFCID3t random identifier to wake up the Target.

Byte 10: DID

The DID byte shall be used for multiple data transport protocol activation with more than one Targets. The range of the DID shall be defined between 1 and 14. The value 0 shall be used, if no DID is used during the data transport protocol. All other values are prohibited by this Standard. The Initiator may assign a different value to the Target, as used before the last DSL command.
12.5.2.2 Wakeup Response (WUP_RES)

This clause defines the Wakeup Response for attributes WUP_RES. The WUP_RES shall be the response to the WUP_REQ and shall be sent by the selected NFCIP-1 Target device.

<table>
<thead>
<tr>
<th>CMD 0</th>
<th>CMD 1</th>
<th>Byte 0</th>
</tr>
</thead>
<tbody>
<tr>
<td>(D5)</td>
<td>(03)</td>
<td>DID</td>
</tr>
</tbody>
</table>

*Figure 36 — Structure of the WUP_RES*

12.5.2.2.1 Definition of the WUP_RES bytes

**CMD 0:** Shall be set to (D5).

**CMD 1: WUP_RES**

The WUP_RES byte shall specify the response to the WUP_REQ. The value of WUP_RES shall be (03).

**Byte 0: DID**

The DID byte shall be used for multiple data exchange protocol activation with more than one Targets. The DIDt shall have the same value as the DIDi. All other values are prohibited by this Standard.

12.5.2.3 Handling of WUP_REQ and WUP_RES

12.5.2.3.1 Initiator rules

When the Initiator has sent a WUP_REQ and receives a valid WUP_RES the Initiator shall continue with operation.

In any other case the Initiator shall retransmit the WUP_REQ before it shall use the deactivation sequence as defined in clause 12.7. In case of failure of the deactivation sequence for passive mode it may use the SLP_REQ command as defined in clause 11.1.2.19 for Passive communication mode at 106 kbps.

12.5.2.3.2 Target rules

When the Target has been de-selected by the last command (for the Active communication mode only) and

a) receives a WUP_REQ with its NFCID3, the Target
   - shall send its WUP_RES and
   - shall disable in order to not receive a subsequent WUP_REQ.

b) receives any other valid or invalid frame, except a SLP_REQ command for passive communication mode at 106 kbps, the Target
   - shall ignore the block and
   - shall remain in receive mode.

12.5.3 Parameter Selection Request and Response Commands

12.5.3.1 Parameter Selection Request (PSL_REQ)

The Initiator may switch parameters for the subsequent transport protocol using the PSL_REQ command.

<table>
<thead>
<tr>
<th>CMD 0</th>
<th>CMD 1</th>
<th>Byte 0</th>
<th>Byte 1</th>
<th>Byte 2</th>
</tr>
</thead>
<tbody>
<tr>
<td>(D4)</td>
<td>(04)</td>
<td>DID</td>
<td>BRS</td>
<td>FSL</td>
</tr>
</tbody>
</table>

*Figure 37 — Structure of the PSL_REQ*

12.5.3.1.1 Definition of the PSL_REQ bytes

**CMD 0:** Shall be set to (D4).
CMD 1: PSL_REQ
The PSL_REQ byte shall specify the command Parameter Selection for the Initiator device. The value of PSL_REQ shall be (04).

Byte 0: DID
The DID shall be similar to the DID defined during ATR or WUP.

Byte 1: BRS
The BRS byte shall specify the selected bit rates for Initiator and Target device.

<table>
<thead>
<tr>
<th>bit 7</th>
<th>bit 6</th>
<th>bit 5</th>
<th>bit 4</th>
<th>bit 3</th>
<th>bit 2</th>
<th>bit 1</th>
<th>bit 0</th>
</tr>
</thead>
<tbody>
<tr>
<td>ZERO</td>
<td>ZERO</td>
<td>DSI</td>
<td>DSI</td>
<td>DSI</td>
<td>DRI</td>
<td>DRI</td>
<td>DRI</td>
</tr>
</tbody>
</table>

*Figure 38 — Coding of the BRS byte*

- bit 7 and bit 6: Shall be set to ZERO.
- bit 5 to bit 3: Bit duration of Initiator to Target.
- bit 2 to bit 0: Bit duration of Target to Initiator.

Table 22 — Coding of DRI and DSI

<table>
<thead>
<tr>
<th>DRI and DSI</th>
<th>Divisor D</th>
</tr>
</thead>
<tbody>
<tr>
<td>000</td>
<td>1</td>
</tr>
<tr>
<td>001</td>
<td>2</td>
</tr>
<tr>
<td>010</td>
<td>4</td>
</tr>
<tr>
<td>011</td>
<td>8</td>
</tr>
<tr>
<td>100</td>
<td>16</td>
</tr>
<tr>
<td>101</td>
<td>32</td>
</tr>
<tr>
<td>110</td>
<td>64</td>
</tr>
<tr>
<td>111</td>
<td>RFU</td>
</tr>
</tbody>
</table>

Byte 2: FSL
The FSL byte defines the maximum value for the Frame Length.

<table>
<thead>
<tr>
<th>bit 7</th>
<th>bit 6</th>
<th>bit 5</th>
<th>bit 4</th>
<th>bit 3</th>
<th>bit 2</th>
<th>bit 1</th>
<th>bit 0</th>
</tr>
</thead>
<tbody>
<tr>
<td>ZERO</td>
<td>ZERO</td>
<td>ZERO</td>
<td>ZERO</td>
<td>ZERO</td>
<td>ZERO</td>
<td>LR</td>
<td>LR</td>
</tr>
</tbody>
</table>

*Figure 39 — Coding of FSL bytes*

- bit 7 to bit 2: Shall be set to all ZERO.
- bit 1 and bit 0: Length Reduction value.

Table 23 — Definition of LR

<table>
<thead>
<tr>
<th>LR</th>
<th>LEN_MAX</th>
</tr>
</thead>
<tbody>
<tr>
<td>00</td>
<td>Only Byte 0 to Byte 63 is valid in the Transport Data</td>
</tr>
<tr>
<td>01</td>
<td>Only Byte 0 to Byte 127 is valid in the Transport Data</td>
</tr>
<tr>
<td>10</td>
<td>Only Byte 0 to Byte 191 is valid in the Transport Data</td>
</tr>
<tr>
<td>11</td>
<td>Only Byte 0 to Byte 255 is valid in the Transport Data</td>
</tr>
</tbody>
</table>
12.5.3.2 Parameter Selection Response (PSL_RES)

The definition of the frame structure of PSL_RES shall be as follows.

<table>
<thead>
<tr>
<th>CMD0</th>
<th>CMD1</th>
<th>Byte 0</th>
</tr>
</thead>
<tbody>
<tr>
<td>(D5)</td>
<td>(05)</td>
<td>DID</td>
</tr>
</tbody>
</table>

*Figure 40 — Structure of PSL_RES*

12.5.3.2.1 Definition of the PSL_RES bytes

**CMD0:** Shall be set to (D5).

**CMD1:** PSL_RES

The PSL_RES byte shall specify the command Parameter Selection response for the Target device. The value of PSL_RES shall be (05).

**Byte 0:** DID

The DID shall be the same as the DID defined during ATR or WUP.

12.5.3.3 Handling of PSL_REQ and PSL_RES

12.5.3.3.1 Initiator rules

The Initiator may change protocol parameters by sending the PSL_REQ to the Target. After reception of a valid PSL_RES, the Initiator

- shall change the framing to the format, which is defined in clause 12.1 and
- shall continue with operation.

In any other case the Initiator may retransmit the PSL_REQ before it shall use the deactivation sequence as defined in clause 12.7. In case of failure of deactivation sequence it may use the SLP_REQ command as defined in clause 11.2.1.27 for the Passive communication mode.

12.5.3.3.2 Target rules

When the Target has received a ATR_REQ, sent its ATR RES and

a) receives a valid PSL_REQ, the Target
   - shall send its PSL_RES,
   - shall disable the PSL_REQ (stop responding to received PSL_REQ),
   - shall change all parameters to the defined values, which are specified in clause 12.5.3 and
   - shall remain in receive mode.

b) receives an invalid frame, the Target
   - shall ignore the block,
   - shall disable the PSL_REQ (stop responding to received PSL_REQ),
   - shall remain with the current framing and
   - shall remain in receive mode.

c) receives a valid frame, except a PSL_REQ, the Target
   - shall disable the PSL_REQ (stop responding to received PSL_REQ),
   - shall remain with the current framing and
   - shall continue operation.
12.6 Data Exchange Protocol

12.6.1 Data Exchange Protocol Request and Response

12.6.1.1 Data Exchange Protocol Request (DEP_REQ) and Response (DEP_RES)

The protocol shall be a half-duplex protocol supporting block-oriented data transmission with error handling. For data, which does not fit in one frame, a chaining mechanism is defined. Format of the protocol frame shall be as follows:

**Transport Data field**

<table>
<thead>
<tr>
<th>CMD 0</th>
<th>CMD 1</th>
<th>Byte 0</th>
<th>Byte 1</th>
<th>Byte 2</th>
<th>Byte 3</th>
<th>Byte 4</th>
<th>...</th>
<th>Byte n</th>
</tr>
</thead>
</table>

**Data Exchange Protocol Header**

<table>
<thead>
<tr>
<th>CMD 0</th>
<th>CMD 1</th>
<th>PFB</th>
<th>[DID]</th>
<th>[NAD]</th>
</tr>
</thead>
</table>

**Transport data bytes**

<table>
<thead>
<tr>
<th>Data byte 0</th>
<th>Data byte 1</th>
<th>...</th>
<th>Data byte n</th>
</tr>
</thead>
</table>

*Figure 41 — Definition of the protocol frames*

In information interchange, the content of the payload of the Transport Data Field requires agreement between the interchanging parties.

12.6.1.1.1 Definition of the Data Exchange Protocol Header bytes

**CMD 0:**
- If the CMD1 is DEP_REQ then the CMD0 shall be set to (D4).
- If the CMD1 is DEP_RES then the CMD0 shall be set to (D5).

**CMD 1: DEP_REQ**

The DEP_REQ bytes specify the command for the data exchange protocol for the Initiator device. The value of the DEP_REQ shall be (06).

**CMD 1: DEP_RES**

The DEP_RES bytes specify the command for data exchange for the Target device. The value of the DEP_RES shall be (07).

**Byte 0: PFB**

The PFB byte shall contain bits to control the data transmission and error recovery. The PFB byte is used to convey the information required controlling the transmission. The data exchange protocol defines three fundamental types of pdu’s:

- Information pdu’s to convey information for the application layer.
- ACK/NACK pdu’s to convey positive or negative acknowledgements. An ACK/NACK pdu never contains a data field. The acknowledgement relates to the last received block.
- Supervisory pdu’s to exchange control information between the Initiator and the Target. Two types of supervisory pdu’s are defined:
  - Timeout extensions containing a 1 byte long data field.
  - Attention containing no data field.

The coding of PFB depends on its type and is defined in the following definitions.
Table 24 — Coding of the PFB bits 7 to 5

<table>
<thead>
<tr>
<th>bit 7</th>
<th>bit 6</th>
<th>bit 5</th>
<th>PFB</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
<td>Information pdu</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>0</td>
<td>ACK/NACK pdu</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>Supervisory pdu</td>
</tr>
</tbody>
</table>

Other settings are RFU

Definition of the Information pdu:

<table>
<thead>
<tr>
<th>bit 7</th>
<th>bit 6</th>
<th>bit 5</th>
<th>bit 4</th>
<th>bit 3</th>
<th>bit 2</th>
<th>bit 1</th>
<th>bit 0</th>
</tr>
</thead>
<tbody>
<tr>
<td>ZERO</td>
<td>ZERO</td>
<td>ZERO</td>
<td>MI</td>
<td>NAD</td>
<td>DID</td>
<td>PNI</td>
<td>PNI</td>
</tr>
</tbody>
</table>

Figure 42 — Coding of the information pdu

- bit 7 to bit 5: Shall be set to all ZEROs.
- bit 4: If bit set to ONE then it indicates Multiple Information chaining activated.
- bit 3: If bit set to ONE then it indicates NAD available.
- bit 2: If bit set to ONE then it indicates DID available.
- bit 1 and bit 0: PNI packet number information.

The Packet Number Information (PNI) counts the number of packet send by the Initiator to the Target and vice versa starting by 0. These bytes are used for error detection during the protocol handling.

Definition of ACK/NACK pdu:

<table>
<thead>
<tr>
<th>bit 7</th>
<th>bit 6</th>
<th>bit 5</th>
<th>bit 4</th>
<th>bit 3</th>
<th>bit 2</th>
<th>bit 1</th>
<th>bit 0</th>
</tr>
</thead>
<tbody>
<tr>
<td>ZERO</td>
<td>ONE</td>
<td>ZERO</td>
<td>ACK/NACK</td>
<td>NAD</td>
<td>DID</td>
<td>PNI</td>
<td>PNI</td>
</tr>
</tbody>
</table>

Figure 43 — Coding of the ACK/NACK pdu

- bit 7: Shall be set to ZERO.
- bit 6: Shall be set to ONE.
- bit 5: Shall be set to ZERO.
- bit 4: If bit set to ONE then it indicates NACK, otherwise ACK.
- bit 3: If bit set to ONE then it indicates NAD available.
- bit 2: If bit set to ONE then it indicates DID available.
- bit 1 and b0: PNI packet number

Definition of the Supervisory pdu (Attention-Target Present, Timeout extensions):

<table>
<thead>
<tr>
<th>bit 7</th>
<th>bit 6</th>
<th>bit 5</th>
<th>bit 4</th>
<th>bit 3</th>
<th>bit 2</th>
<th>bit 1</th>
<th>bit 0</th>
</tr>
</thead>
<tbody>
<tr>
<td>ONE</td>
<td>ZERO</td>
<td>ZERO</td>
<td>Attention/</td>
<td>NAD</td>
<td>DID</td>
<td>ZERO</td>
<td>ZERO</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>Timeout</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Figure 44 — Coding of the supervisory pdu

- bit 7: Shall be set to ONE.
- bit 6 and bit 5: shall be set to ZERO.
- bit 4: If ATTENTION then set to ZERO. If TIME-OUT_EXTENTION then set to ONE.
- bit 3: If bit set to ONE then it indicates NAD available.
- bit 2: If bit set to ONE then it indicates DID available.
- bit 1 and b0: Shall be set to ZERO.

**Byte 1: DID**
The DID byte shall be the same as defined during activation of the protocol.

**Byte 2: NAD**
The NAD byte is reserved to build up and address different logical connections on both the Initiator and the Target device. Bit 7 to bit 4 code the logical address of the Initiator, bits 3 to 0 code the logical address of the Target. The following definitions shall apply for the usage of the NAD.
- The NAD shall only be used for the data exchange protocol.
- When the Initiator uses an NAD, the Target shall also use an NAD.
- If MI bit is set, the NAD shall only be transmitted in the first frame.
- The Initiator shall never use the NAD to address two different Targets.

**Byte 3 to Byte n: User data bytes**
The data field shall contain the transported data and is optional. When present, it conveys either application data or status information. The length of the data field is calculated by subtracting the mandatory and optional send bytes of the data exchange transport header from the length byte and additionally subtracting one.

12.6.1.2 Handling of Pdu number information

12.6.1.2.1 Initiator rules
The PNI of the Initiator shall be initialized for each Target with all ZEROs. When an information or acknowledge pdu with an equal PNI is received, the Initiator shall increment the current PNI for that Target before optionally sending a new frame.

12.6.1.2.2 Target rules
The PNI of the Target shall be initialized with all ZEROs. When an information or acknowledge pdu with an equal PNI was received the Target shall send its response with this PNI and shall increment the PNI afterwards.

12.6.1.3 Handling of Blocks

12.6.1.3.1 General rules
The first pdu shall be sent by the Initiator. When a data pdu indicating more information is received the pdu shall be acknowledged by an ACK pdu.
Supervisory pdus are only used in pairs. A Supervisory Request shall always be followed by a Supervisory Response.

12.6.1.3.2 Initiator rules
When an invalid pdu was received a NACK pdu shall be sent (except in the case of DSL or RLS).
When a timeout occurs, an attention command shall be sent (except a NACK has been sent before).
When a timeout occurs and a NACK has been sent before, the NACK shall be retransmitted.
When an ACK pdu is received, if its pdu number is equal to the current PNI of the Initiator, the chaining shall be continued.

If the DSL_REQ is not answered by a valid DSL_RES the DSL_REQ may be retransmitted or the Target command ignored.

12.6.1.3.3 Target rules

The Target is allowed to send an RTO pdu instead of a data pdu.

When a data pdu not containing chaining is received it shall be acknowledged by a data pdu.

When an NACK pdu is received, if the PNI is equal to the PNI of the previous sent pdu, the previous block shall be re-transmitted.

When an erroneous pdu is received the Target shall not answer but stay in same State.

When a Supervisory pdu coding an attention command is received, the Target shall respond sending a supervisory attention pdu response.

12.6.2 Response timeout extension

The response timeout extension shall only be used by the Target. When a Target needs more time to process the received block from the Initiator than defined in RWT, it shall use a supervisory pdu using response timeout extension request. A response timeout extension request contains 1 byte long data field. The definition of the byte is shown in Figure 45.

<table>
<thead>
<tr>
<th>Bit 7</th>
<th>bit 6</th>
<th>bit 5</th>
<th>Bit 4</th>
<th>bit 3</th>
<th>bit 2</th>
<th>bit 1</th>
<th>bit 0</th>
</tr>
</thead>
<tbody>
<tr>
<td>ZERO</td>
<td>ZERO</td>
<td>RTOX</td>
<td>RTOX</td>
<td>RTOX</td>
<td>RTOX</td>
<td>RTOX</td>
<td>RTOX</td>
</tr>
</tbody>
</table>

Figure 45 — Coding of Response timeout extension byte

- bit 7 and bit 6: Shall be set to ZERO.
- bit 5 to bit 0: RTOX value.

For RTOX the values 0 and 60 to 63 are RFU. For all other values the intermediate RWT_{INT} shall be calculated by the following formula:

\[
RWT_{INT} = RWT \times RTOX
\]

The RWT_{INT} starts after the Initiator has sent its RTOX response to the Target. In case RWT_{INT} exceeds RWT_{MAX}, RWT_{MAX} shall be used. The RWT_{INT} is valid until the next frame has been received by the Initiator.

12.6.3 Attention – Target present

The Initiator shall send the attention command to the Target to ensure the Target is still in field for passive mode or to be able to detect a Target loss during multi-activation. This command shall not change the current State of the Target.

The Target shall respond to a valid Attention Request sending an attention command containing the identical data field to the Initiator.

If the Target receives an incorrect pdu it shall not respond and shall stay in the same State.

12.6.4 Protocol operation

After the activation sequence the Target shall wait for a block as only the Initiator has the right to send. After sending a block, the Initiator shall switch to receive mode and wait for a block before switching back to transmit mode. The Target may transmit blocks only in response to received blocks. After responding, the Target shall return to the receive mode.

The Initiator shall not initiate a new pair of Request/Response until the current pair of Request/Response has been completed or if the frame waiting time is exceeded without response.
12.6.5 Multi Activation

The Multi-Activation feature allows the Initiator to hold several Targets active simultaneously. It allows switching directly between several Targets without needing additional time for deactivation of a Target and activation of another Target.

For an example of Multi-Activation see Table 25. The Initiator needs to handle a separate package number information for each activated Target.

Table 25 — Multi Activation

<table>
<thead>
<tr>
<th>Initiator Action</th>
<th>Status Target 1</th>
<th>Status Target 2</th>
<th>Status Target 3</th>
</tr>
</thead>
<tbody>
<tr>
<td>Choose active mode</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Activate Target 1 with DID=1</td>
<td>Selected (1)</td>
<td>Sense</td>
<td>Sense</td>
</tr>
<tr>
<td>Any communication with DID=1</td>
<td>Selected (1)</td>
<td>Sense</td>
<td>Sense</td>
</tr>
<tr>
<td>...</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Activate Target 2 with DID=2</td>
<td>Selected (1)</td>
<td>Selected (2)</td>
<td>Sense</td>
</tr>
<tr>
<td>Any communication with DID=1,2</td>
<td>Selected (1)</td>
<td>Selected (2)</td>
<td>Sense</td>
</tr>
<tr>
<td>...</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Activate Target 3 with DID=3</td>
<td>Selected (1)</td>
<td>Selected (2)</td>
<td>Selected (3)</td>
</tr>
<tr>
<td>Any communication with DID=1,2,3</td>
<td>Selected (1)</td>
<td>Selected (2)</td>
<td>Selected (3)</td>
</tr>
<tr>
<td>...</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Deactivation Sequence with DID=1</td>
<td>Sleep</td>
<td>Selected (2)</td>
<td>Selected (3)</td>
</tr>
<tr>
<td>Any communication with DID=2,3</td>
<td>Sleep</td>
<td>Selected (2)</td>
<td>Selected (3)</td>
</tr>
<tr>
<td>...</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Deactivation Sequence with DID=2</td>
<td>Sleep</td>
<td>Sleep</td>
<td>Selected (3)</td>
</tr>
<tr>
<td>Any communication with DID=3</td>
<td>Sleep</td>
<td>Sleep</td>
<td>Selected (3)</td>
</tr>
<tr>
<td>...</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Deactivation Sequence with DID=3</td>
<td>Sleep</td>
<td>Sleep</td>
<td>Sleep</td>
</tr>
<tr>
<td>...</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

12.6.6 More information (Chaining)

The chaining feature allows the Initiator or Target to transmit information that does not fit in a single block, by dividing the information into several blocks. Each of those blocks shall have a length less than or equal to the maximum frame size ($\text{LEN}_{\text{MAX}}$).

The chaining bit in the PFB of a protocol frame control the chaining of frames. Each frame with the chaining bit set shall be acknowledged by an ACK pdu.

The chaining feature is shown in Figure 46 using a 16 bytes long string transmitted in three blocks.

12.7 Deactivation of the protocol

After exchange of data by using the data exchange protocol, the Initiator may apply a deactivation of the data exchange protocol. After successful deactivation Initiator and Target shall stay in the initially chosen mode, but the Initiator may choose one of the defined bit rates for reactivation.

The reactivation of Targets is different for passive and active mode and is defined in clause 11.2.1.16 for passive mode and in clause 12.5.2 in active mode.

After successful deactivation the Target shall not respond to subsequent ATR_REQ commands.
The RLS_REQ command shall switch the Target back to POWER ON State. See 12.7.2.1. In this State, the Target shall answer to all initial communication schemes and shall also answer to an ATR_REQ.

12.7.1 Deselect Request and Response command

12.7.1.1 Deselect request (DSL_REQ)

This clause defines the deselect command DSL_REQ. The DSL_REQ is sent from Initiator to the Target.

12.7.1.1.1 Definition of DSL_REQ bytes

CMD 0: Shall be set to (D4).

CMD 1: DSL_REQ

The DSL_REQ byte specify the command deselect for the Initiator device. The value of DSL_REQ shall be (08).

Byte 0: DID

The DID shall be the same as defined during ATR or WUP commands.

12.7.1.2 Deselect response (DSL_RES)

This clause defines the Deselect Response command DSL_RES. The DSL_RES is the response to the DSL_REQ and is sent from the Target to the Initiator.
12.7.1.2.1 Definition of Deselect Response bytes

CMD 0: Shall be set to (D5).

CMD 1: DSL_RES

The DSL_RES byte specifies the command deselect response for the Target device. The value of DSL_RES shall be (09).

Byte 0: DID

The DID shall be the same as in DSL_REQ.

12.7.1.3 Handling of DSL_REQ and DSL_RES

12.7.1.3.1 Initiator rules

When the Initiator has sent a DSL_REQ and received a valid DLS_RES, the Target has been successfully halted. The DID assigned to the Target has been released.

12.7.1.3.2 Target rules

When the Target has received a DSL_REQ and sent its DSL_RES, the Target
– shall stay in initially chosen mode,
– shall enable to receive the default bit rates defined in clause 11.2 for the Passive communication mode and in clause 11.3 for the Active communication mode.
– shall remain in receive mode until a valid ALL_REQ is received in passive communication mode at 106 kbps or a WUP_REQ is received in active communication mode.

12.7.2 Release Request and Response commands

12.7.2.1 Release Request (RLS_REQ)

This clause defines the release command RLS_REQ. The RLS_REQ is sent from the Initiator to the Target.

<table>
<thead>
<tr>
<th>CMD0</th>
<th>CMD1</th>
<th>Byte 0</th>
</tr>
</thead>
<tbody>
<tr>
<td>(D4)</td>
<td>(0A)</td>
<td>[DID]</td>
</tr>
</tbody>
</table>

Figure 49 — Structure of RLS_REQ

12.7.2.1.1 Definition of RLS_REQ bytes

CMD 0: Shall be set to (D4).

CMD 1: RLS_REQ

The RLS_REQ bytes specify the command release for the Initiator device. The value of the RLS_REQ bytes shall be (0A).

Byte 0: DID

The DID shall be the same as defined in ATR or WUP commands.

12.7.2.2 Release response RLS_RES

This clause defines the answer to release command the release response command RLS_RES. The RLS_RES is the response to the RLS_REQ sent from the Target to the Initiator.
12.7.2.2.1 Definition of RLS_RES bytes

CMD 0: Shall be set to (D5).

CMD 1: RLS_RES

The RLS_RES bytes specify the command release for the Target device. The value of the RLS_RES bytes shall be (0B).

Byte 0 : DID

The DID shall be the same as defined in RLS_REQ command.

12.7.2.3 Handling of RLS_REQ and RLS_RES

12.7.2.3.1 Initiator rules

When the Initiator has sent a RLS_REQ and received a valid RLS_RES, the Target has been successfully released. The Initiator may return to initial State.

12.7.2.3.2 Target rules

When the Target has received a RLS_REQ and sent its RLS_RES, the Target shall return to initial State.
Annex A
(normative)

CRC calculation

A.1 CRC for Active and Passive communication mode at 106 kbps

The frame CRC shall be a function of \( k \) data bits, which consist of all the data bits in the frame, excluding parity bits, S and E, and the CRC itself. Since data is encoded in bytes, the number of bits \( k \) shall be a multiple of 8. For error checking, the two CRC bytes shall be sent in the Standard Frame, after the bytes and before the E.

The CRC shall be calculated by the following polynomial. The pre-set value shall be (6363) and the register content shall not be inverted after calculation.

\[ G(x) = x^{16} + x^{12} + x^5 + 1 \]

For an example of the CRC calculation for the Active and Passive modes at 106 kbps, see A.2.

A.2 Example of CRC calculation at 106 kbps

This example is provided for explanatory purposes and indicates the bit patterns that will exist in the physical layer. It is included for the purpose of checking the Passive communication mode implementation at 106 kbps encoding.

The process of encoding and decoding may be conveniently carried out by a 16-stage cyclic shift register with appropriate feedback gates. According to ITU-T Recommendation (ITU-T V.41, Code-independent error control system), Annex I, figures I-1/V.41 and I-2/V.41 the flip-flops of the register shall be numbered from FF0 to FF15. FF0 shall be the leftmost flip-flop where data is shifted in. FF15 shall be the rightmost flip-flop where data is shifted out. Table A.1 defines the initial content of the register.

Table A.1 — Initial content of 16-stage shift register according to value (6363)

<table>
<thead>
<tr>
<th>FF0</th>
<th>FF1</th>
<th>FF2</th>
<th>FF3</th>
<th>FF4</th>
<th>FF5</th>
<th>FF6</th>
<th>FF7</th>
<th>FF8</th>
<th>FF9</th>
<th>FF10</th>
<th>FF11</th>
<th>FF12</th>
<th>FF13</th>
<th>FF14</th>
<th>FF15</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>1</td>
</tr>
</tbody>
</table>

Consequently, FF0 corresponds to the msb and FF15 to the lsb.

Examples of bit patterns that will be transmitted via Standard Frames:

Example 1: Transmission of data, first byte = (00), second byte = (00), CRC appended.

Calculated CRC = (1EA0)

First bit transmitted

```
S 0000 0000 1 0000 0000 1 0000 0101 1 0111 1000 1 E
(00) P (00) P (A0) P (1E) P
```

Figure A.1 — Example 1 for CRC encoding

Table A.2 — Content of 16-stage shift register according to value (1EA0)

<table>
<thead>
<tr>
<th>FF0</th>
<th>FF1</th>
<th>FF2</th>
<th>FF3</th>
<th>FF4</th>
<th>FF5</th>
<th>FF6</th>
<th>FF7</th>
<th>FF8</th>
<th>FF9</th>
<th>FF10</th>
<th>FF11</th>
<th>FF12</th>
<th>FF13</th>
<th>FF14</th>
<th>FF15</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td></td>
</tr>
</tbody>
</table>

Example 2: Transmission of data block, first byte = (12), second byte = (34), CRC appended.

Calculated CRC = (CF26)

First bit transmitted.
Figure A.2 — Example 2 for CRC encoding

Table A.3 — Content of 16-stage shift register according to value (CF26)

<table>
<thead>
<tr>
<th>FF0</th>
<th>FF1</th>
<th>FF2</th>
<th>FF3</th>
<th>FF4</th>
<th>FF5</th>
<th>FF6</th>
<th>FF7</th>
<th>FF8</th>
<th>FF9</th>
<th>FF10</th>
<th>FF11</th>
<th>FF12</th>
<th>FF13</th>
<th>FF14</th>
<th>FF15</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>0</td>
</tr>
</tbody>
</table>

A.3 CRC for Active and Passive communication mode at 212 kbps and 424 kbps

The CRC shall be calculated by the CCITT CRC-16, the scope of which shall include the Length field and the Payload field. Calculation in a $G(x)$ shall be defined by:

$$G(x) = x^{16} + x^{12} + x^5 + 1$$

Pre-set value shall be 0. For an example of the CRC calculation for the Active and Passive modes at 212 kbps and 424 kbps, see A.4.

A.4 Example of CRC calculation at 212 kbps and 424 kbps

The sample Frame is as follows: (00) (00) (00) (00) (00) (00) (B2) (4D) (03) (AB) (CD) (90) (35) (B2) (4D) is SYNC. (03) is the length. (AB) (CD) is the user data. (90) (35) is the corresponding CRC.