ECMA

EUROPEAN COMPUTER MANUFACTURERS ASSOCIATION

STANDARD ECMA-44

FOR

THE IMPLEMENTATION OF THE ECMA 7-BIT AND 8-BIT CODED CHARACTER SETS ON PUNCHED CARDS

September 1975

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BRIEF HISTORY

When ECMA adopted their Standard ECMA-6 for the 7-Bit Coded Character Set for Information Interchange, no proposals for implementation in media were included. These implementations were deliberately left as the subject for future standards.

In 1969 Standard ECMA-20 was published which defined the implementation of the 7-bit coded character set on 12-row punched cards. A year later, in 1970, Standard ECMA-25, an extension of Standard ECMA-20 to an abstract relationship between hole-patterns and the 256 possible 8-bit combinations, was published. With the development within ECMA of code extension techniques (ECMA-35) and of an 8-bit coded character set, it was felt that the existence of these two standards as separate documents was no more justified.

The present Standard ECMA-44 supersedes both, ECMA-20 and ECMA-25. However, its technical content is identical to that of these Standards.

ADOPTED BY THE GENERAL ASSEMBLY OF ECMA ON JUNE 19, 1975.

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1. SCOPE

1.1 The present Standard defines the implementation of the ECMA Coded Character Sets, which are the subject of the following Standards:

- ECMA-6 : defines a set of 128 characters and their

coded representation using 7 bits.

- ECMA-35: defines the techniques to be utilized in

order to enlarge the repertoire of the

standard character set of ECMA-6.

- ECMA-43: defines a set of 256 characters and their

coded representation using 8 bits.

1.2 This Standard specifies the representation of 7-bit combinations and 8-bit combinations on 12-row punched cards. This representation is derived from, and compatible with the code known as "Hollerith Code". It will ensure compatibility with a large proportion of existing punched card files.

It does not specify any redundancy nor does it define techniques for error control.

2. FIELD OF APPLICATION

This Standard is intended for the general interchange of information among data processing systems, when using 12-Row Punched Cards as data carrier.

3. PUNCHED CARDS

3.1 A punched card, as referred to in this Standard, is an interchange medium (document) in which characters are represented in successive columns, each column having twelve possible punching positions.

This Standard does not define the number of columns in a card, nor the shape of the holes punched in a card, nor any of its other physical characteristics. The above features are the subject of other international or national standards.

Note: See for example:

- ISO Recommendation R 1681, Specification for unpunched paper cards.
- ISO Recommendation R 1682, Dimensions and location for rectangular punched holes in 80 columns punched paper cards.

3.2 Twelve possible punching positions are available in each column of the card. For the purpose of this Standard, they are designated as:

12 11 0 1 2 3 4 5 6 7 8 9

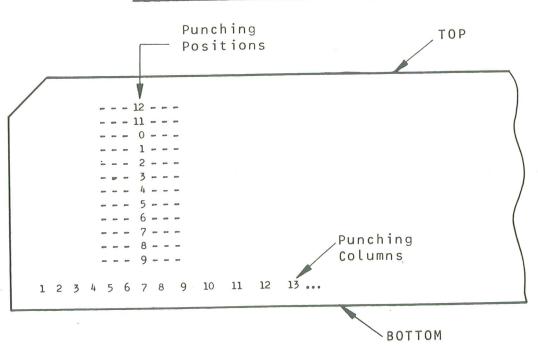
The hole patterns specified in this Standard are formed by punching the twelve available positions of a given column of a punched card in the following way:

- positions 12 11 0 9 8 may be punched in any combination, giving $2^5 = 32$ possibilities,
- only one of positions 1 2 3 4 5 6 7 may be punched, which together with the NO PUNCH condition gives 8 possibilities.

These rules provide for 256 hole patterns (32 \times 8).

Fig. I

LAYOUT OF PUNCHING POSITIONS



4. SPECIFICATION

4.1 Implementation of the 8-Bit Coded Character Set

Table 1 specifies for each of the 256 8-bit combinations the corresponding hole-pattern.

The columns and rows are numbered in binary and decimal notation. Each position in Table 1 is identified by its column and row number (e.g. 06/11) and by the corresponding bit combination (e.g. 0110 1011). The entries in the table are card hole-patterns.

4.2 Implementation of the 7-Bit Coded Character Set

The hole-patterns shown in the first half of the table (i.e. columns 00 to 07) apply to the 128 7-bit combinations, which are obtained ignoring the most significant eighth bit (bg).

			Т			1									1				-	
_	-	_		_ 5	12 11 9 8 4		12 11 8 8	12 11 9 8 7	11 0 8 8 2	110	1110	1110	11 0		12 11 0 9 8 2	12 11 0	12 11 9 8 4	12 11 0 9 8 5	12 11 11 10 10 10 10 10 10 10 10 10 10 10	12 11 0 8 7
_	_	_	0	1 4	12 11 0	11 0	12 11 0 8 2	12 11 0 8 3	12 11 0 8 4	12 11 0 8 5	12 11 0 8 6	12 11 0 8 7	12 9 8 2	12 9 8 3	12 9 8 4	12 0 8 9 5	12 0	12 0 8 7		9 11 3
_	-	0	-	<u>ල</u>	12 11 8 7	1110	111 0	111 0	11 0		111 0		12 11 0	12 11 0	12 11 0	12 11 0	12 11 0	12 11 0	12 11 0	12 11 0 7
_	-	0	0	1 2	12 11 0	112 11 0	12 11 0 9 8	12 0	12 0 8 2	12 0 8 3	12 0	12 0 8 5	12 0 8 6	12 0 1 8 7	12 11 8 1	12 11 8 2	12 11 8 3	12 11 8 4	12 11 8 5	12 11 8 6
_	0	_	-	_	112 11	11	11 0	11 0	111 0	11 0	111 0	11 0	11 0	8 1	12 11 0	12 11 0	12 11 0	11	12 11 0	=
_	0	_	0	0	12 0		12 0	12 0	12 0		12 0	(00)	12 8 1	12 11	12 11 2	12 11 9 3	12 11 9	12 11 9 5	12 11 6	12 11 5
_	0	0	-	60	12 11 0	9 1	11 9 8 2	9	9	9	9	12 9 8	8	9 8 1	982	8 8	12 9 4	11	9 8 6	110
_	0	0	0	08	11 0 8 6	0 1	0 6	0 6	9 4	11 9	12 6	11	0 8 6	0 8 1	9 8 2	0 8 6	9 8 4	12 8 1	12 9 8 2	9 8 3
0	_	1	-	07	12 11	12 11	12 11	11 0	110	110	110	11 0	11 0	110	11 0	12 0	12 11	11 0	110	12 9 7
0	_	-	0	90	8	12 0	12 0	12 0	12 0	12	12 0	12 0 .	12 0	9	12 11	12 11 2	12 11	12 11	12 11 5	12 11
0	_	0	-	0 5	11	8 11	9	002	0 6	0 4	0 5	09	0	0	0	12 8 2	8	11	11 8 7	8
0	_	0	0	0 4	80	12	12	12	12	12	12	12	12	12	=======================================	11	=======================================	11	11	11
0	0	-	-	80	0	-	[2]	m	4	ľ	9	7	80	6	80	8 6	12 8 4	80	8	0 8 7
0	0	_	0	02		12 8 7	8 7	(m)	(3) (3)	8 4	[2]	8	12 8 5		111	12 8 6	8	E	12	0=
0	0	0	_	0	12 11	11	11	11	8 8	8 8	2	0 9	6 11 8	11 8 1	7 8 6	0 6	118	11 8 8 5	9 8 6	11 8 6
0	0	0	0	0	12 0 8 1 1	12		12	7 6	0 8 6	0 8 6	0 8 6		E E S S		12 8 8 3	12 8 8	12 8 8 5	12 9 8 6	12 9 8 7
Ps	6,	Pe	P s		00	0	0 2	က ()	0 4	0 5	90	07	∞ Ο	6 0	0	_	- 2	<u>ෆ</u>	1 4	- 5
				٥	0	_	0	_	O	_	0	_	0	_	0	_	0	_	0	_
				P 2	0	0	_	-	0	0	-	_	0	0		eness	0	0	_	_
				b ₄ b ₃	0	0	0	0	-	_	_	_	0	0	0	0	_	_	_	_
				٥	0	0	0	0	0	0	0	0	_	_		_	_			_

APPENDIX A

For ease of reference, and with a view to helping the reader, the correspondence defined in Table 1 between 256 8-bit combinations and 256 hole-patterns is shown hereafter in another form in Table 2.

The layout of Table 2 is as follows:

- The columns, the left half of the rows and the right half of the rows are labelled with card hole-patterns;
- The entries in the table represent the column/row position of a bit-combination of the 8-bit code table.

Example: The card hole-pattern 12-11-2 corresponds to position 06/11 of the code table (i.e. to bit pattern 0110 1011).

Table 2

BIT-COMBINATIONS ASSIGNED TO CARD HOLE-PATTERNS

	_		2	3	4	5	9	7			[2]	3	4	5	9	7
	8	6	6	6	6	9	6	6	9 8	9 8	8	9 8	8	8	8	8
12 11 0	13/08	11/11	11/12	11/13	11/14	11/15	12/00	12/01	12/02	00/60	15/10	15/11	15/12	15/13	15/14	15/15
11	13/01	09/15	11/02	11/03	11/04	11/05	11/06	11/07	11/08	00/80	15/04	15/05	15/06	15/07	15/08	15/09
12 11	12/10	10/09	10/10	10/11	10/12	10/13	10/14	10/15	11/00	01/00	14/14	14/15	15/00	15/01	15/02	15/03
12 0	12/03	10/00	10/01	10/02	10/03	10/04	10/05	10/06	10/01	00/00	14/08	14/09	14/10	14/11	14/12	14/13
	00/90	10/60	01/06	80/60	09/04	50/60	90/60	00/04	80/60	60/60	01/60	09/11	01/04	01/05	09/14	01/10
0	11/09	08/01	08/02	08/03	08/04	00/10	01/07	01/11	80/80	60/80	08/10	08/11	08/12	90/00	90/00	20/00
Ξ	11/01	01/01	01/02	01/03	09/13	90/80	80/00	20/80	01/08	01/09	20/60	08/15	01/12	01/13	01/14	01/15
12	10/08	10/00	00/05	20/00	09/12	60/00	90/80	07/15	20/60	08/13	08/14	00/11	00/12	00/13	00/14	00/15
12 11 0	11/10	13/09	13/10	13/11	13/12	13/13	13/14	13/15	14/00	14/01	14/02	14/03	14/04	14/05	14/06	14/07
=	07/13	07/14	02/03	07/04	90/20	90/20	20/20	80/20	60/20	01/10	13/02	13/03	13/04	13/05	13/06	13/07
12 11	07/12	06/10	06/11	06/12	06/13	06/14	06/15	00/20	10/20	07/02	12/11	12/12	12/13	12/14	12/15	13/00
12	07/11	10/90	06/02	60/90	06/04	50/90	90/90	20/90	80/90	60/90	12/04	12/05	12/06	12/07	12/08	12/09
	00/20	03/01	03/02	03/03	03/04	03/05	90/80	03/07	80/80	60/80	03/10	02/03	04/00	02/07	03/13	02/02
0	03/00	02/15	05/03	05/04	20/20	90/50	20/50	05/08	60/50	05/10	05/12	02/12	02/05	05/15	03/14	03/15
Ξ	02/13	04/10	04/11	04/12	04/13	04/14	04/15	00/90	10/50	05/02	05/13	02/04	02/10	05/09	03/11	05/14
12	02/06	04/01	04/02	04/03	04/04	04/05	04/06	04/07	04/08	04/09	05/11	02/14	03/12	02/08	02/11	02/01
		-	2		4	5	9	7	<u>∞</u>	6	8 2	8	8 4	8 5	9	8 7

APPENDIX B

CONSIDERATION OF NATIONAL USAGES

For some national versions of the 7-bit or 8-bit codes, the implementation specified in this Standard results in character/hole-pattern relations which, for a few characters, may differ from established practice.

Appendix C

ECMA 7-bit Coded Character Set (ECMA-6)

b ₇				0	0	0	0	1	1	1	1	
b. b.					-	0	1	1	0	0	1	1
D ₅							0	1	0	1	0	1
b	b ₃	b ₂	b			1	2	3	4	5	6	7
0	0	0	0	0	NUL	TC,	SP	0	3	P	(0)	p
0	0	0	1	1	T C ₁	D C	1	1	А	Q	a	q
0	0	1	0	2	T C ₂	D C2	6	2	В	R	b	r
0	0	1	1	3	T C ₃	D C ₃	£(#)	3	С	S	C	S
0	1	0	0	4	TC ₄	D C ₄	\$(¤)	4	D	T	d	t
0	1	0	1	5	TC ₅	TC _a	%	5	Е	U	е	u
0	1	1	0	6	TC.	TC.	&	6	F	V	f	V
0	1	1	1	7	BEL	T C ₁₀ (E T B)	6	7	G	W	g	W
1	0	0	0	8	FE _o	CAN	(8	Н	X	h	X
1	0	0	1	9	FE,	EM)	9	I	Y	i	У
1	0	1	0	10	F E ₂	SUB	*	:	J	Z	j	Z
1	0	1	1	11	F E ₃	ESC	+		K	3	k	3
1	1	0	0	12	FE ₄ (FF)①	IS ₄	/ ₆	<	L	3	l	3
1	1	0	1	13	FE ₅ (CR)①	IS ₃	_=	=	M	3	m	3
1	1	1	0	14	S O	IS ₂		>	N	A 00	n	-
1	1	1	1	15	SI	IS,	/	?	0	Caste	0	DEL

APPENDIX D

ECMA 8-Bit Coded Character Set (ECMA-43)

				b ₈	Contraction of the last	THE R. P. LEWIS CO., LANSING	The second second	The second second	0	0	0	0	1	1	1	1	1	1	1	1
				b,	0	0	0	0	1	0	1	1	0	0	0	0	1	0	1	1
				b _s	.0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1
h	b₃	h.	h		00	01	02	03	04	05	06	07	08	09	10	11	12	13	14	15
0	0	0	0	00	NUL	TC,	SP	0	3	Р	4	р			8					
0	0	0	1	01	T C ₁ (SOH)	D C.	!	1	А	Q	a	q								-
0	0	1	0	02	T C ₂	D C ₂	6	2	В	R	b	r								
0	0	1	1	03	T C ₃	D C ₃	£ (#)	3	С	S	С	S								
0	1	0	0	04	T C ₄	D C4	\$(¤)	4	D	Т	d	t								
0	1	0	1	05	T C 5 (ENQ)	T C ₈	%	5	Е	U	е	u								
0	1	1	0	06	T C ₆	TC.	&	6	F	V	f	V								
0	1	1	1	07	BEL	T C ₁₀ (E T B)	6	7	G	W	g	W								
1	0	0	0	80	FE _o (BS)	CAN	(8	Н	X	h	Х								
1	0	0	1	09	FE,	EM)	9	Ι	Υ	i	У								
1	0	1	0	10	① F E ₂ (LF)	SUB	*	:	J	Z	j	Z								
1	0	1	1	11	① FE ₃ (VI)	ESC	+	" //	K	3	k	3			_					
1	1	0	0	12	FE ₄	IS ₄	6,	<	L	3	l	3								
1	1	0	1	13	FEs (CR)	IS ₃	-	=	M	3	m	3								
1	1	1	0	14	S O	IS ₂		>	N	A (4)(6)	n	46						- 1		100000
1	1	1	1	15	Ŝ I	IS,	/	?	0	-	0	DEL								8
						2.10								9	-			<u> </u>		

